black hat EUROPE 2018

DECEMBER 3-6, 2018

EXCEL LONDON / UNITED KINGDOM

DIVIDE ET IMPERA: MEMORY RANGER RUNS DRIVERS IN ISOLATED KERNEL SPACES

Igor Korkin, Ph.D

🕈 #BHEU / @BLACK HAT EVENTS

WHOAMI

• MEPhI Alumni, PhD in Cyber Security, published 23 papers

- Area of interest is Windows Kernel security:
 - Memory Forensics
 - Rootkits Detection
 - Bare-Metal Hypervisors
- Fan of academic cross-disciplinary research igorkorkin.blogspot.com
- Love traveling and powerlifting ③ igor.korkin

AGENDA

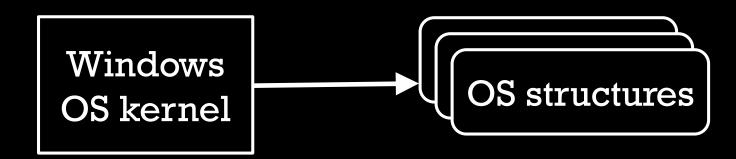
Attacking the kernel-mode memory

Existing protection: Windows built-in security and research projects

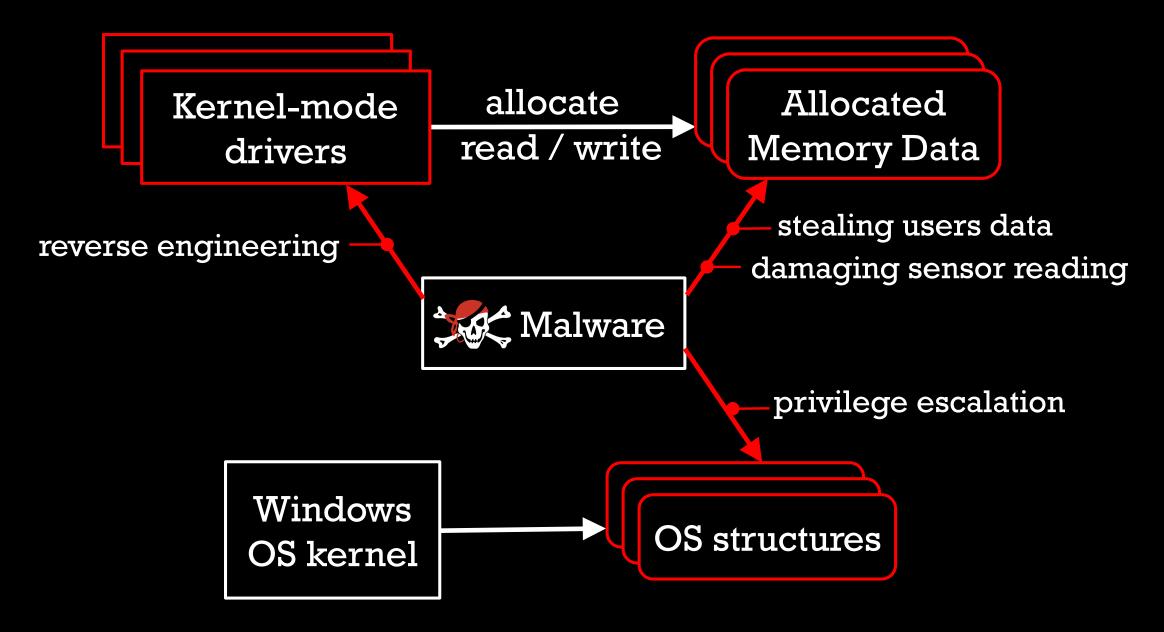
MemoryRanger hypervisor: idea, details, demos

ATTACKS ON KERNEL MODE MEMORY





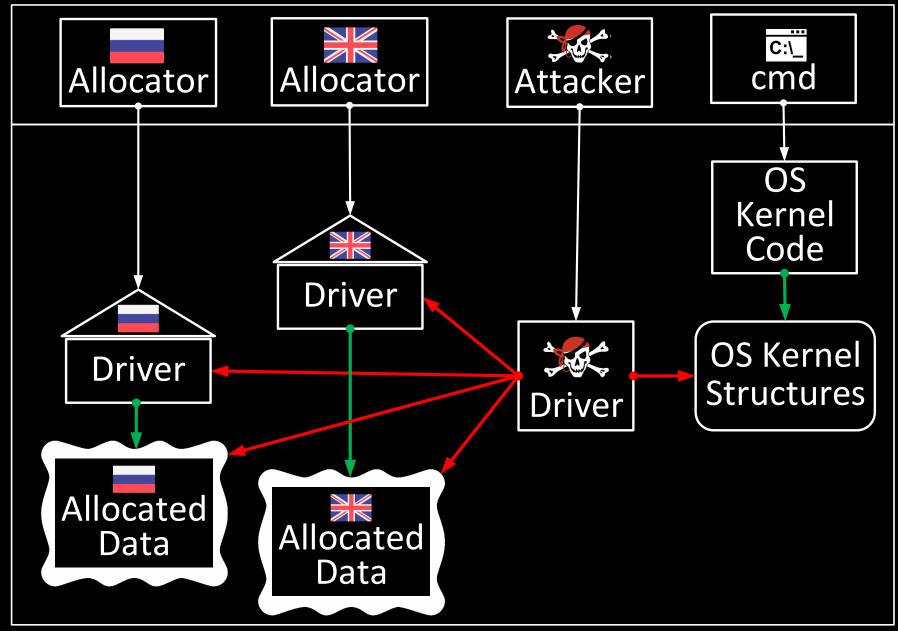
ATTACKS ON KERNEL MODE MEMORY



TWO HOUSES WITH PRIVATE ART COLLECTIONS



DEMO: THE ATTACK

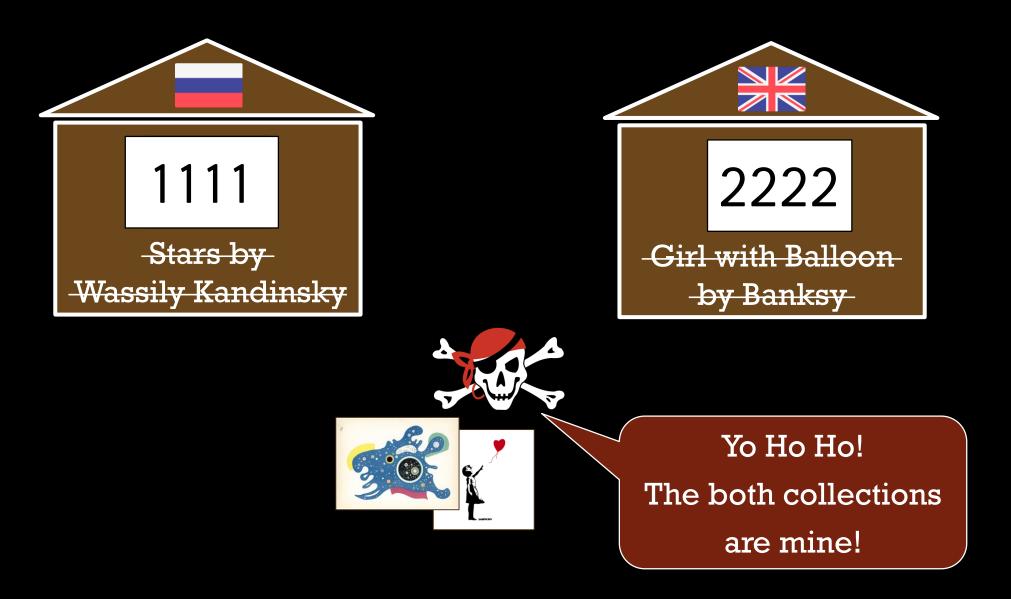


DEMO: THE ATTACK

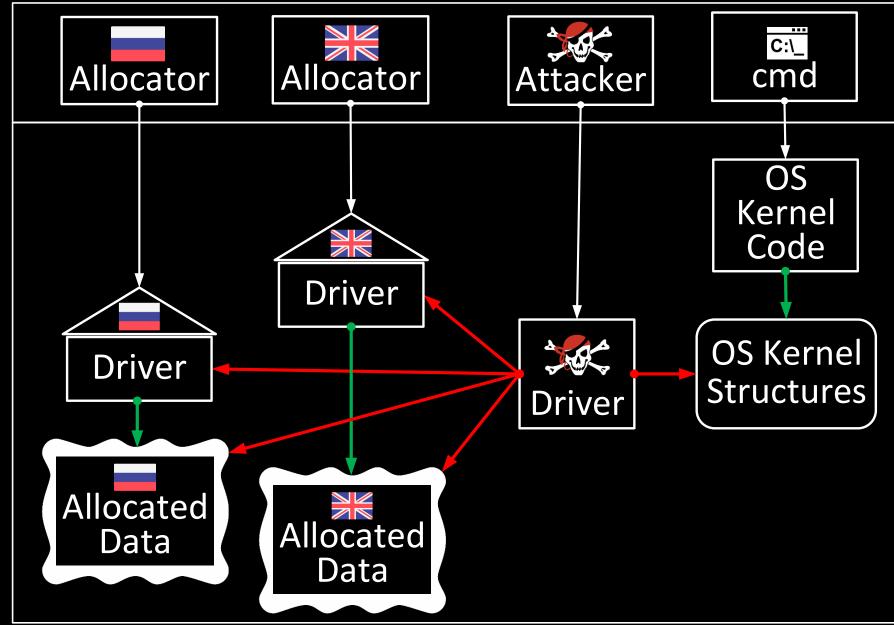
The online version is here –

https://www.youtube.com/embed/HNxc-tjy3QA?vq=hd1080

TWO HOUSES WITH PRIVATE ART COLLECTIONS







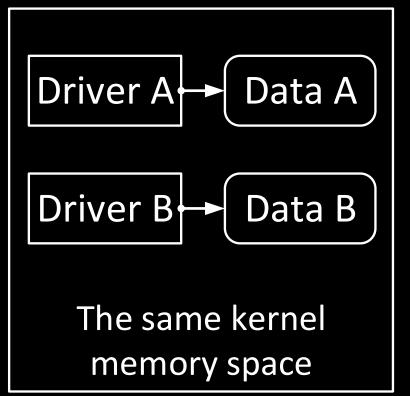
BACKGROUND ANALYSIS

Memory protection projects	Malware attacks on					
	Code of OS &		OS data:		Data of third-	
	third-party drivers		internal structures		party drivers	
	Read	Write	Read	Write	Read	Write
Windows Security	-	BSOD 0xBE by Device Guard	-	BSOD 0x109 by PatchGuard	_	_
PrivGuard	_	_	_	+	_	_
LAKEED	+	+	+	+	_	_
LKMG	_	+	+	+	+	+
rR^X	+	+	-	_	_	_
AllMemPro	_	_	+	+	+	+
Memory Ranger	+	+	+	+	+	+

IDEA OF DRIVERS EXECUTION ISOLATION

Now all drivers share

the same memory space



IDEA OF DRIVERS EXECUTION ISOLATION

Now all drivers share

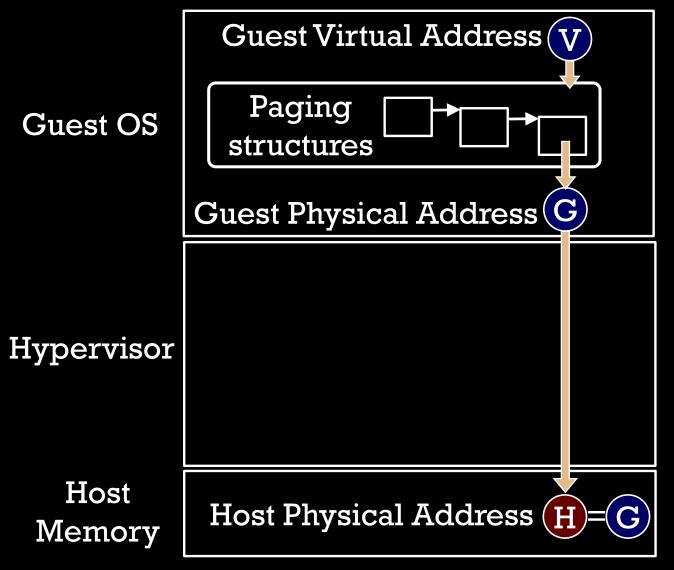
the same memory space

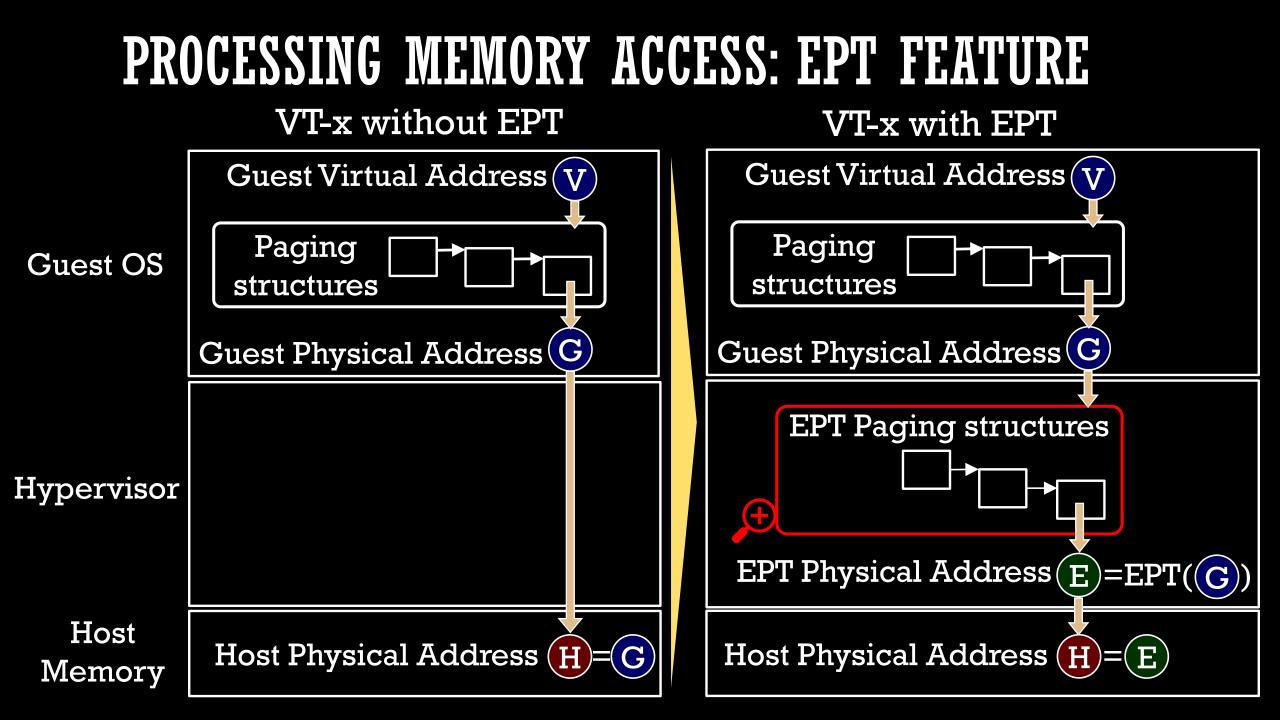
Let's execute these two drivers into separate memory enclosures

Driver A Driver A Data A Data A Driver B Driver B+ Data B Data B Memory enclave only Memory enclave only The same kernel for Driver B for Driver A memory space

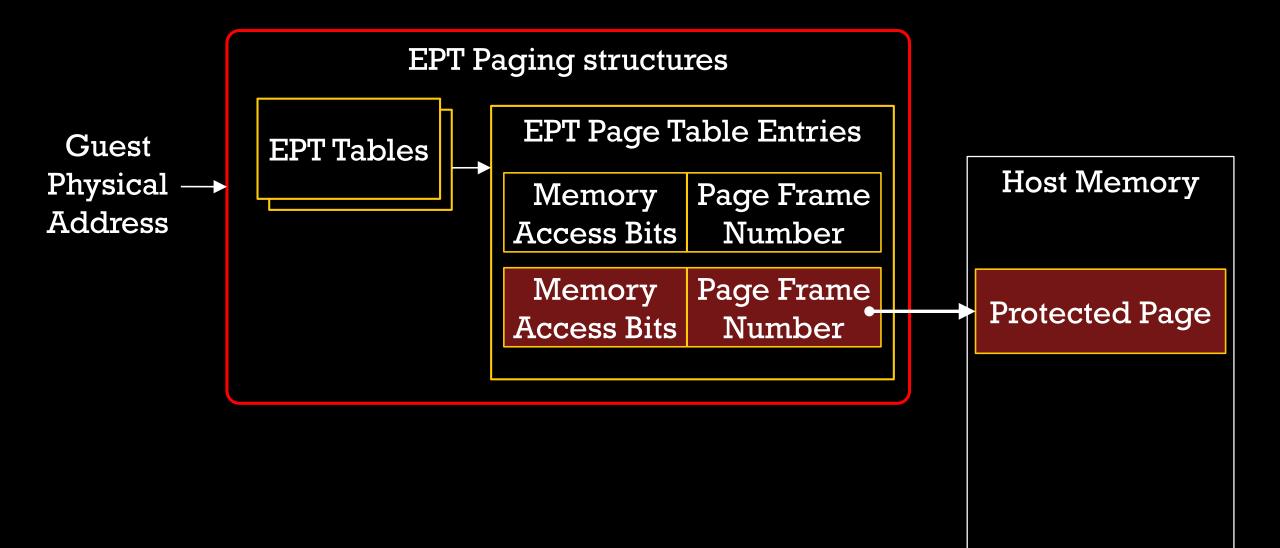
PROCESSING MEMORY ACCESS: EPT FEATURE

VT-x without EPT

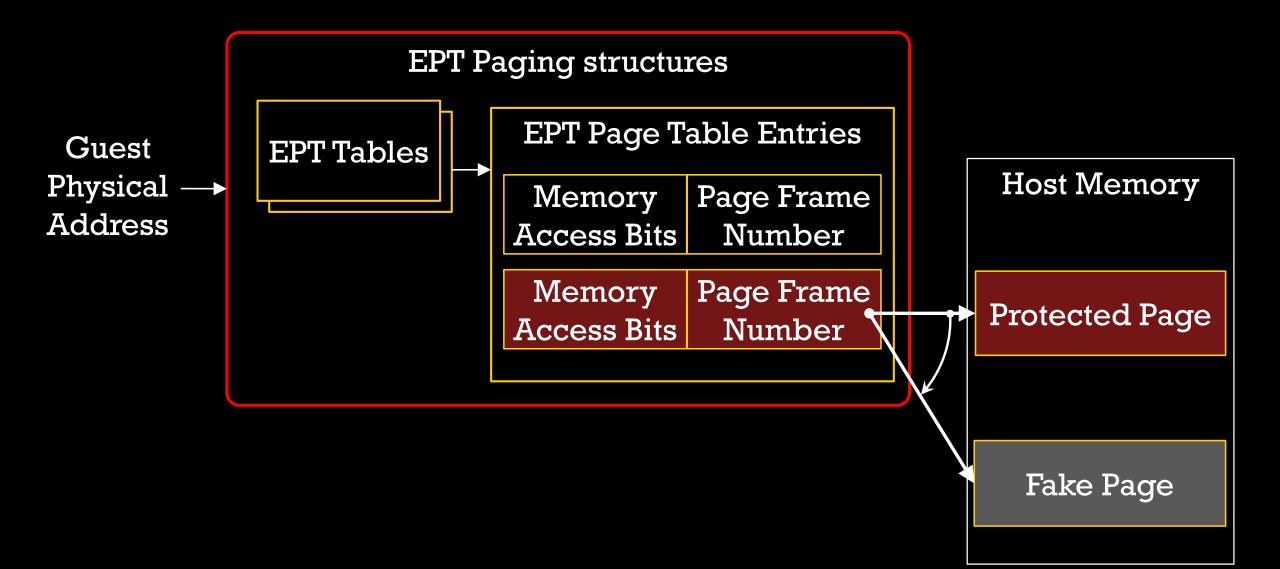




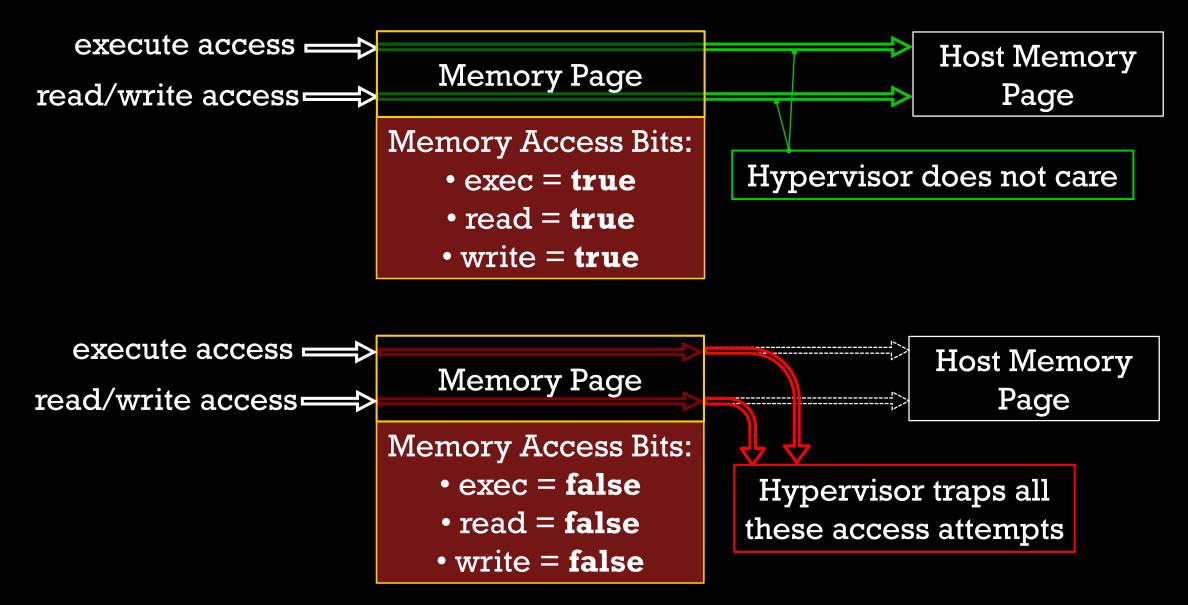
INSIDE EPT PAGING STRUCTURES. EPT PFN



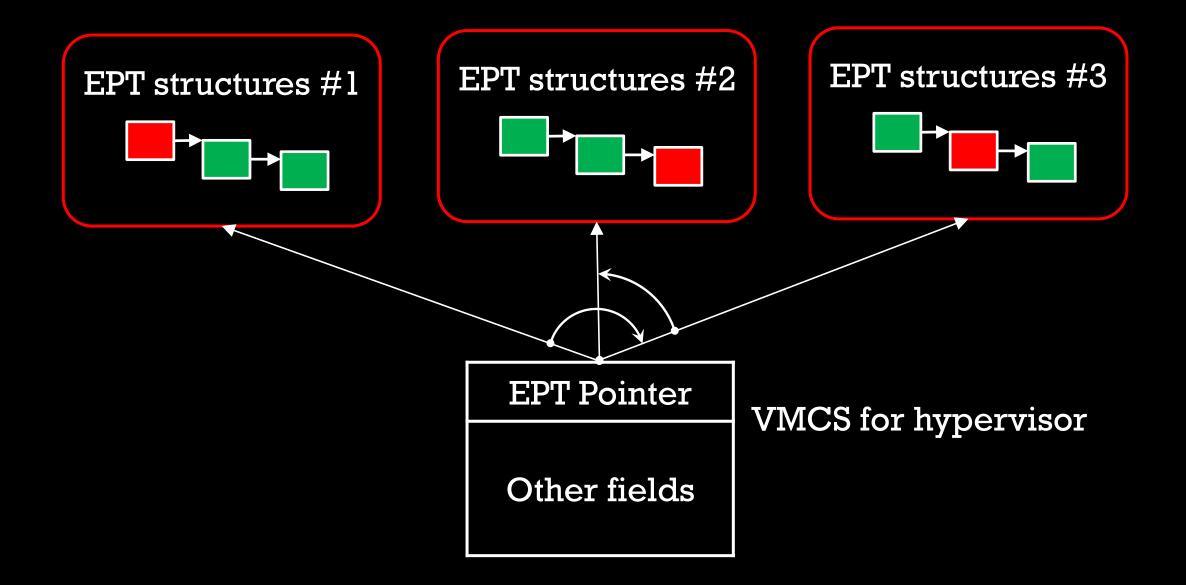
INSIDE EPT PAGING STRUCTURES. EPT PFN

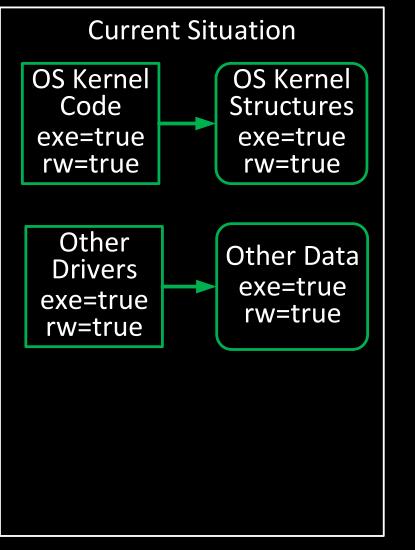


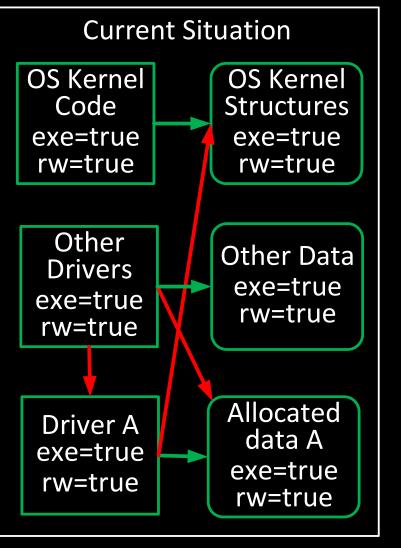
INSIDE EPT PAGING STRUCTURES. EPT BITS

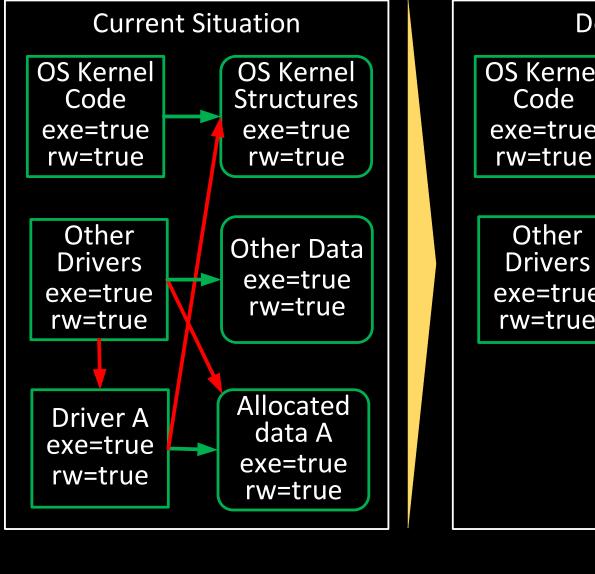


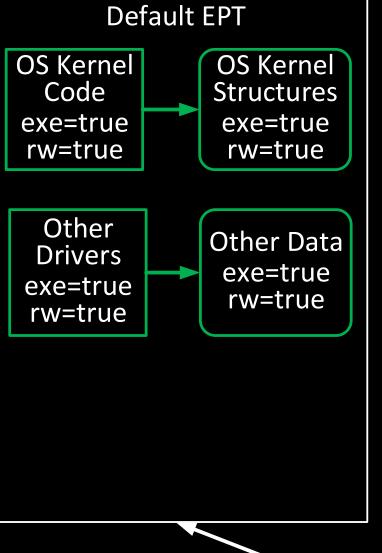
INSIDE EPT PAGING STRUCTURES



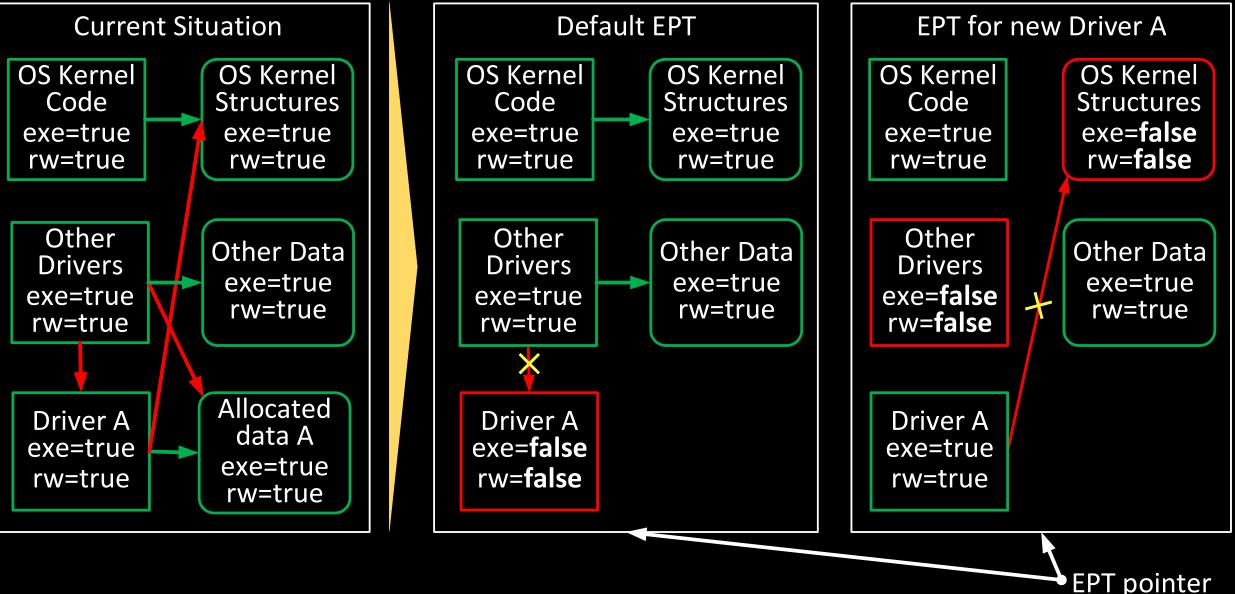


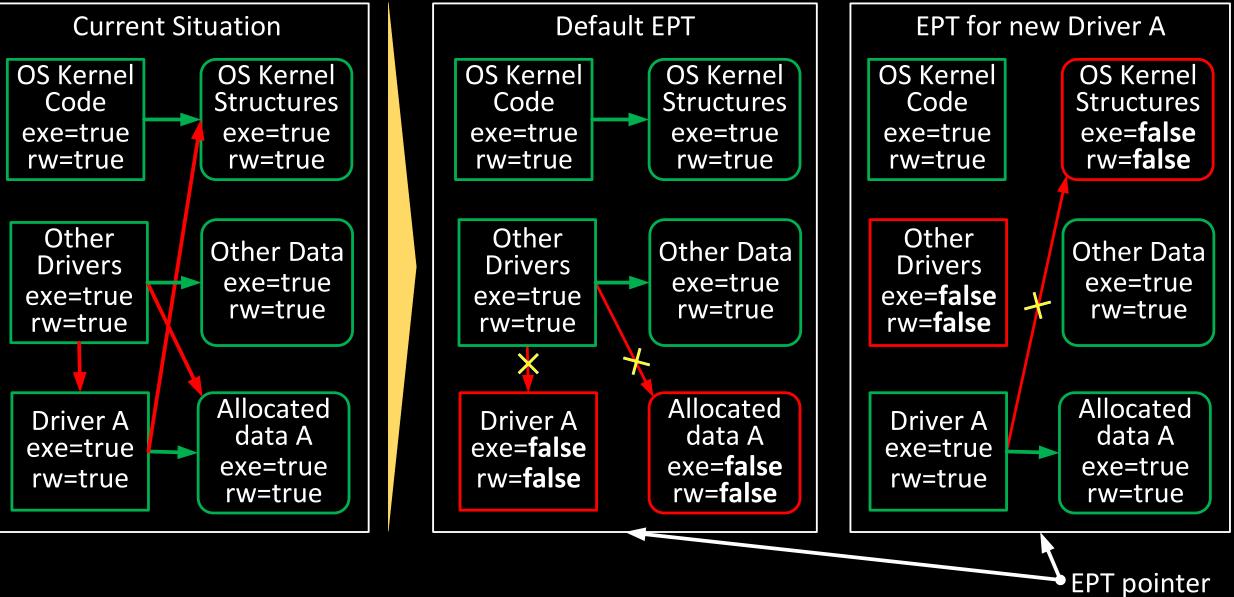




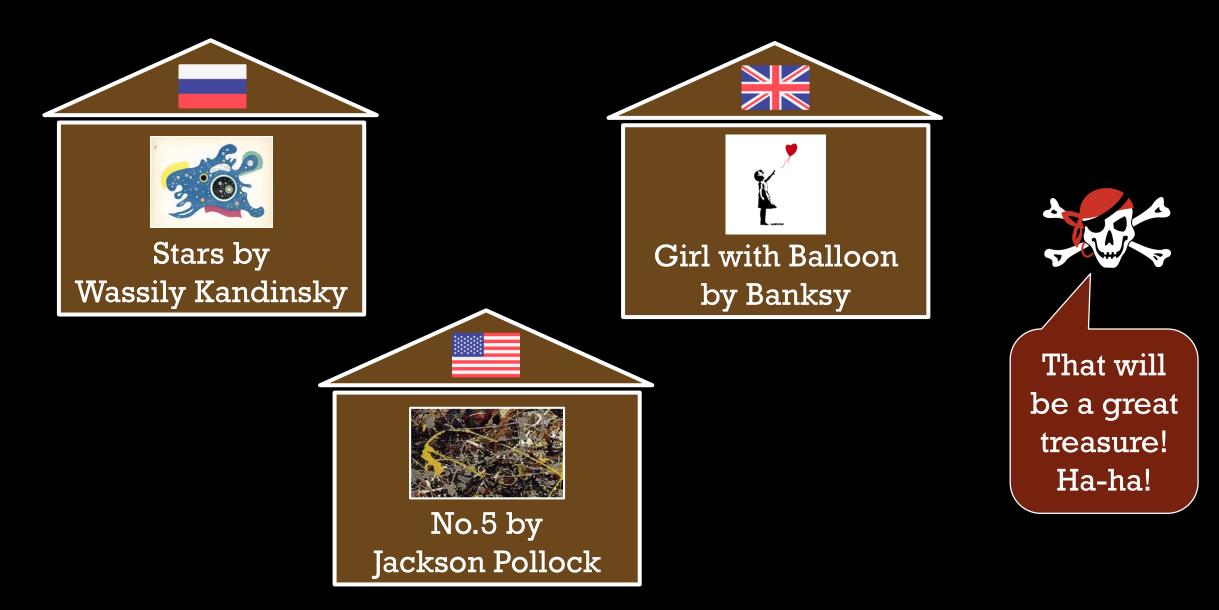


EPT pointer

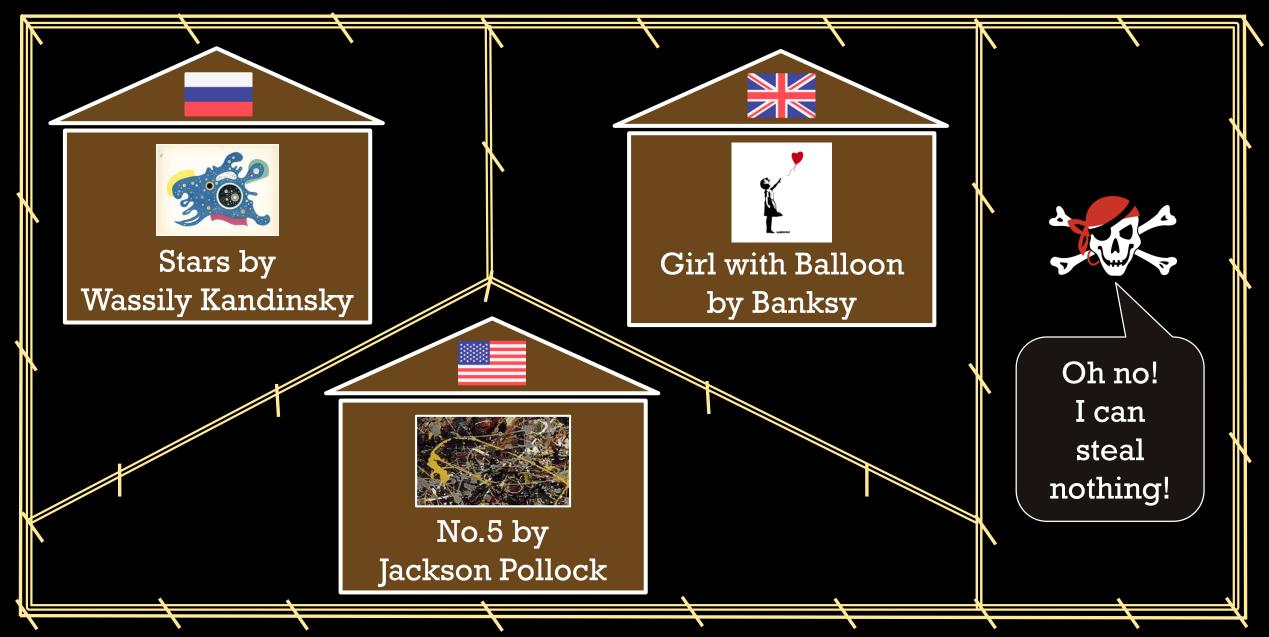




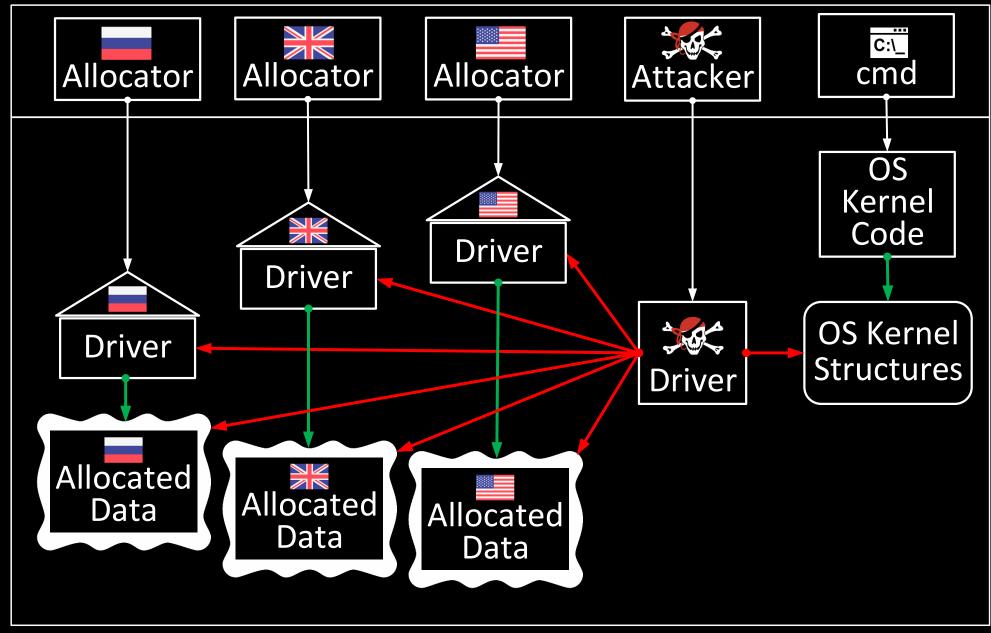
THREE HOUSES WITH PRIVATE ART COLLECTIONS



THREE HOUSES WITH PRIVATE ART COLLECTIONS



DEMO: THE ATTACK PREVENTION

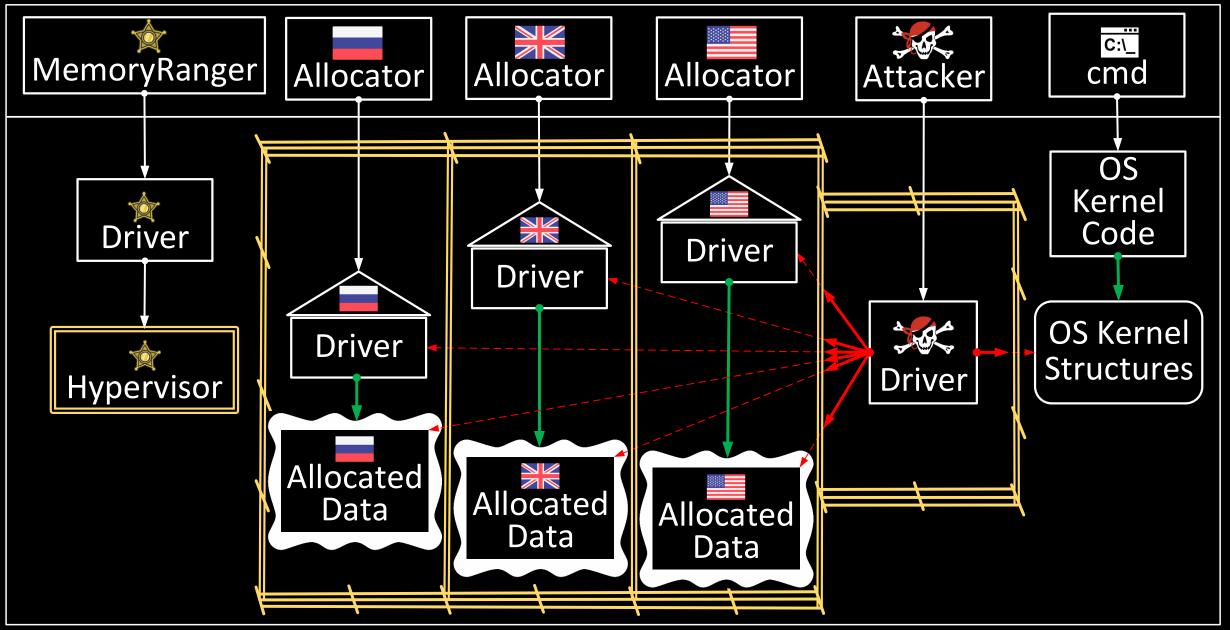


DEMO: THE ATTACK PREVENTION

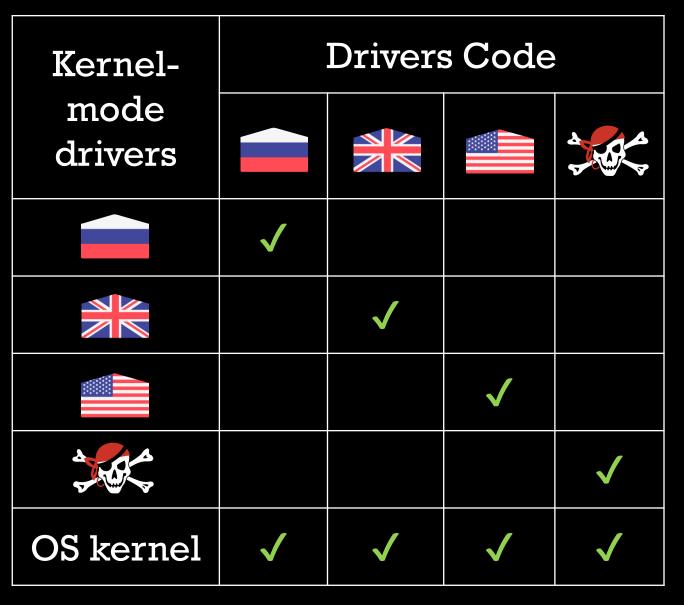
The online version is here –

https://www.youtube.com/embed/vrm9cgn5DsU?vq=hd1080

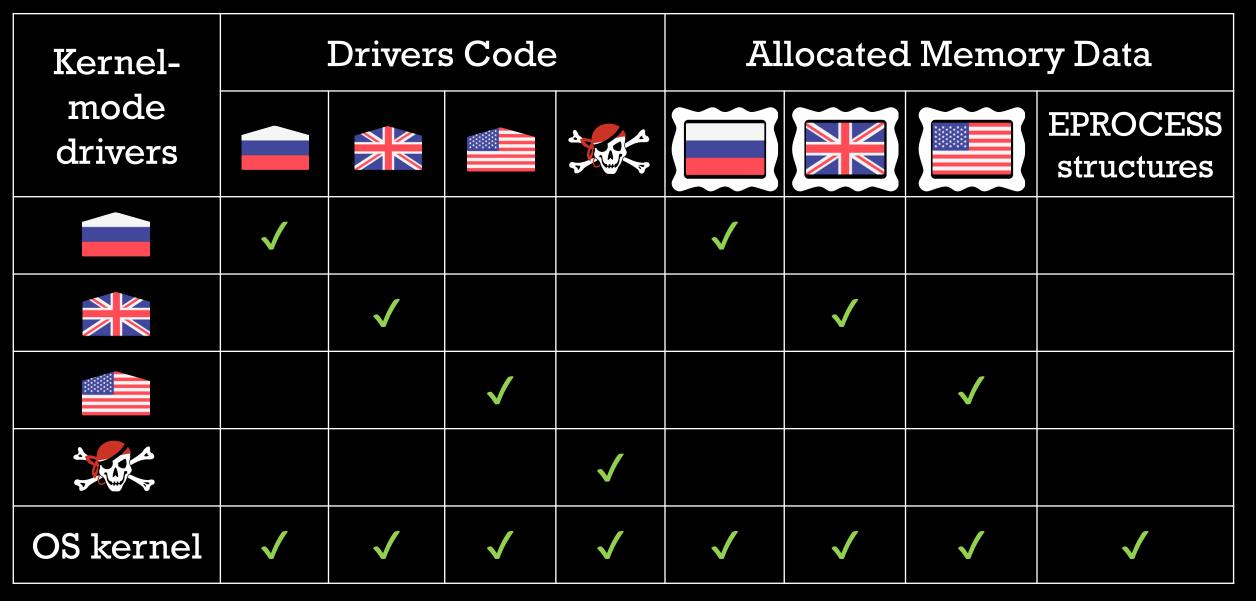
DEMO: THE ATTACK PREVENTION



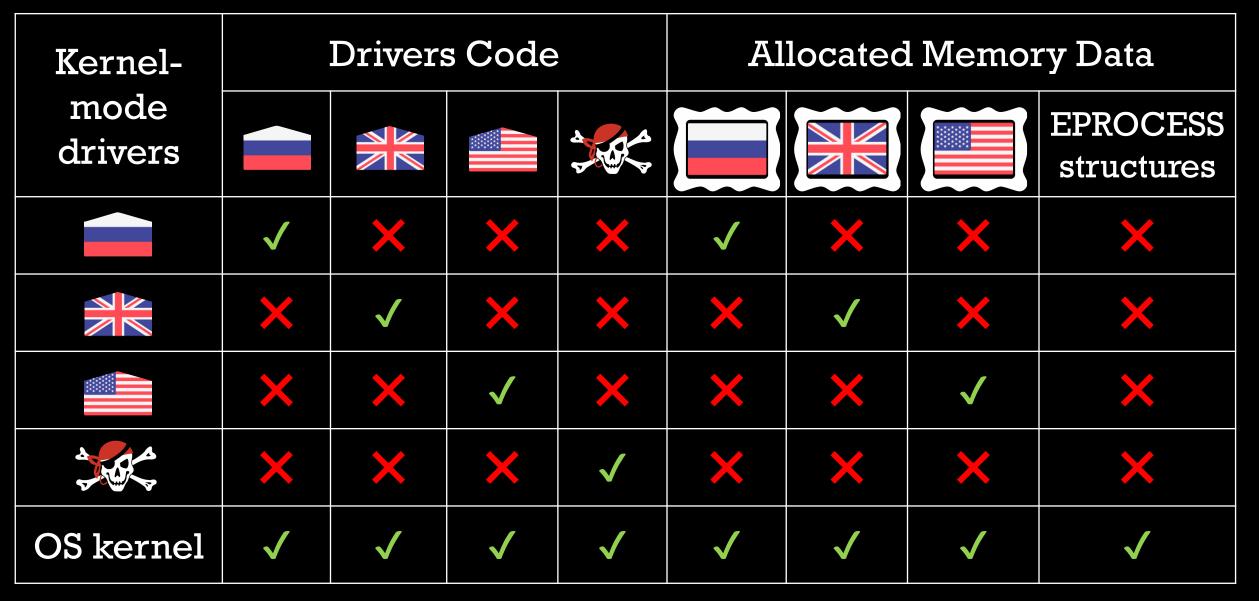
MEMORY RANGER: PRINCIPLE OF LEAST PRIVILEGE

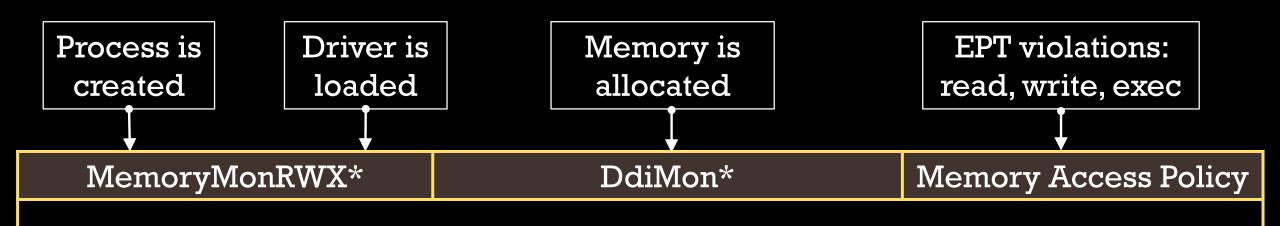


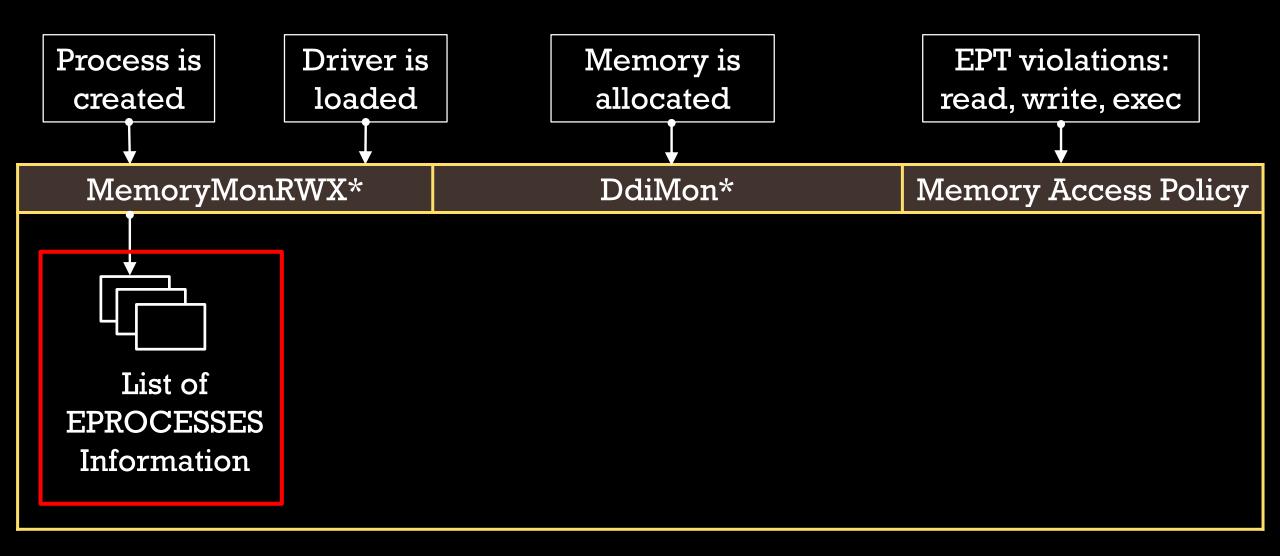
MEMORY RANGER: PRINCIPLE OF LEAST PRIVILEGE

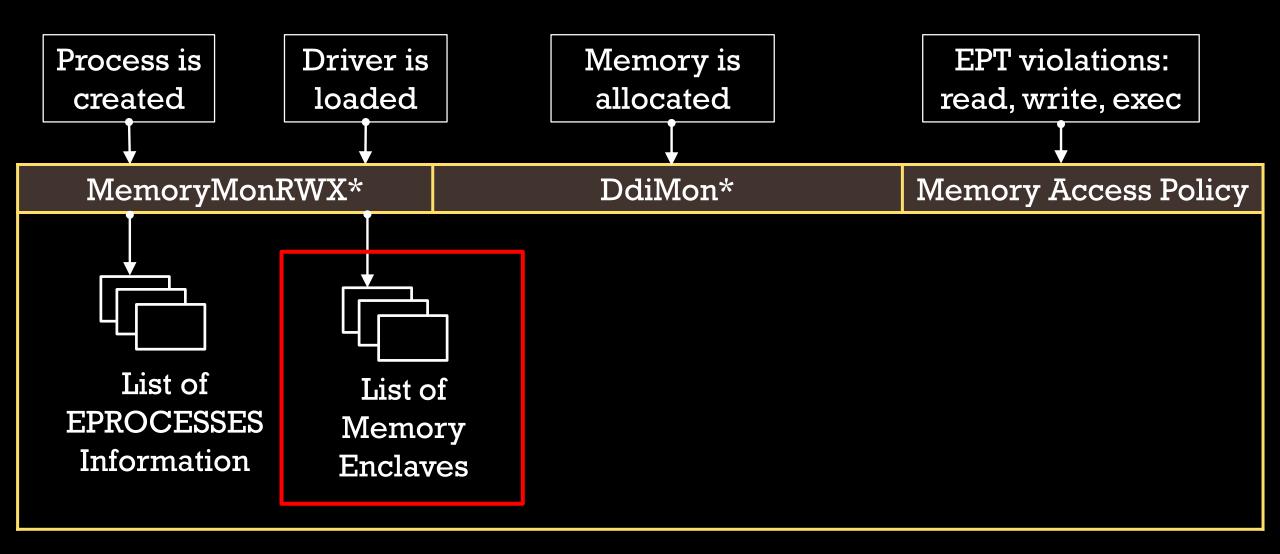


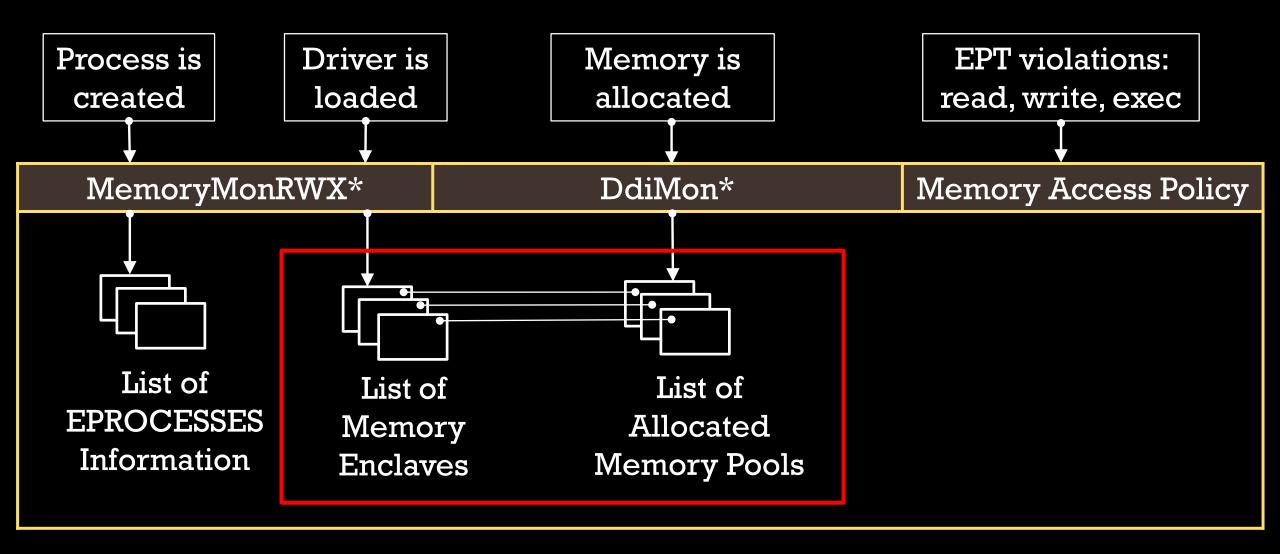
MEMORY RANGER: PRINCIPLE OF LEAST PRIVILEGE

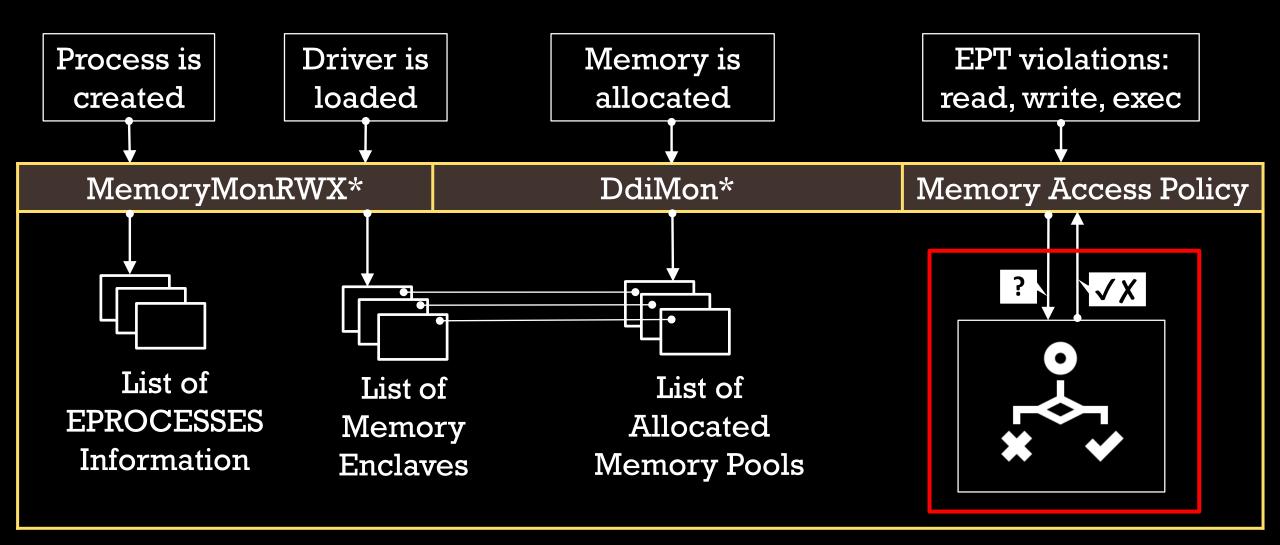












```
switch (exit_reason){
      case (execute_violation):
            change_ept();
            break;
      case (read_violation | | write_violation):
            if (access_legal()==false){
                   set_pte(pfn, read|write, fake_page);
                   set_monitor_trap_flag();
                   break;
      case (monitor_trap_flag):
            set_pte(pfn, no_access, original_page);
            clear_monitor_trap_flag();
            break;
```

switch (exit_reason){

```
case (execute_violation):
      change_ept();
      break;
case (read_violation | | write_violation):
      if (access_legal()==false){
            set_pte(pfn, read|write, fake_page);
            set_monitor_trap_flag();
            break;
case (monitor_trap_flag):
      set_pte(pfn, no_access, original_page);
      clear_monitor_trap_flag();
      break;
```

```
switch (exit_reason){
      case (execute_violation):
            change_ept();
             break;
      case (read_violation | | write_violation):
            if (access_legal()==false){
                   set_pte(pfn, read|write, fake_page);
                   set_monitor_trap_flag();
                   break;
```

case (monitor_trap_flag):
 set_pte(pfn, no_access, original_page);
 clear_monitor_trap_flag();
 break;

```
switch (exit_reason){
    case (execute_violation):
        change_ept();
        break;
    case (read_violation|| write_violation):
        if (access_legal()==false){
            set_pte(pfn, read|write, fake_page);
            set_monitor_trap_flag();
            break;
```

```
case (monitor_trap_flag):
    set_pte(pfn, no_access, original_page);
    clear_monitor_trap_flag();
    break;
```

HOW TO PROTECT YOUR DATA IN MEMORY?

- 1. Callback creating a list of protected objects
 - Add objects' addresses & sizes to the list
 - Restrict memory access for objects memory via EPT

2. EPT dispatcher – processing EPT violations for this data

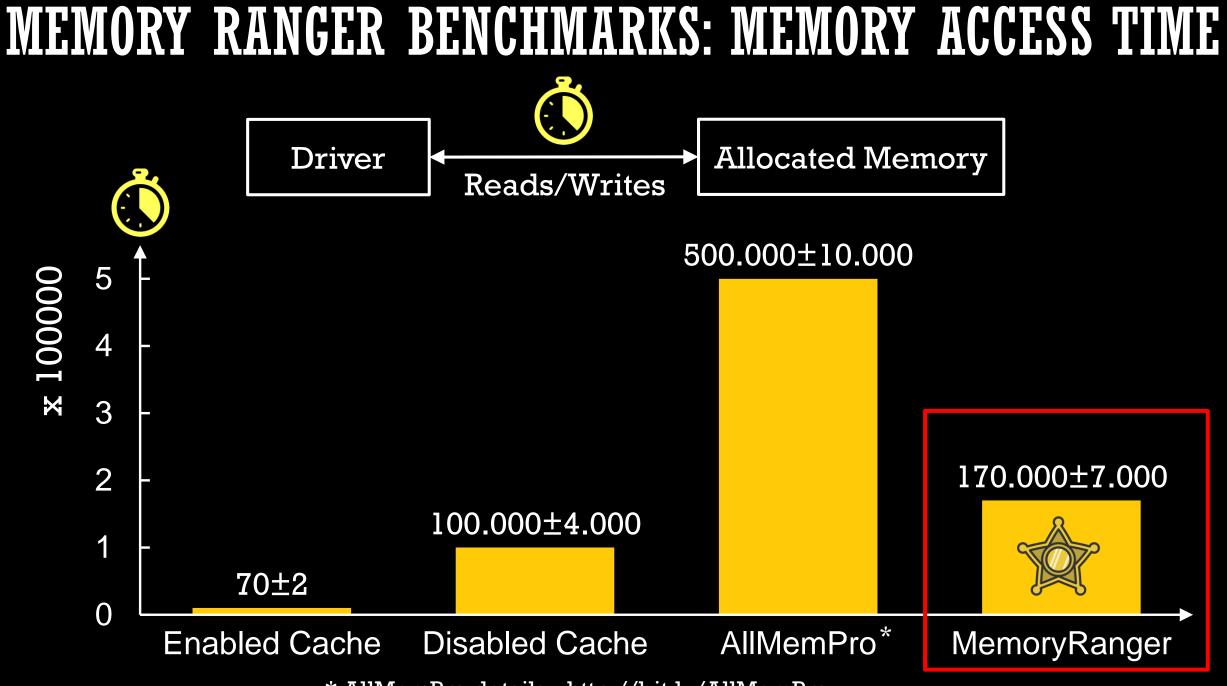
- type_of_access read or write
- guest_ip is the 'source address'
- fault_va is the 'destination address'
- Temporary allow access to the data using MTF
- Redirect access to the fake data using MTF and EPT.PFN

HOW TO PROTECT YOUR DATA IN MEMORY?

- 1. Callback creating a list of protected objects
 - Add objects' addresses & sizes to the list
 - Restrict memory access for objects memory via EPT

2. EPT dispatcher – processing EPT violations for this data

- type_of_access read or write
- guest_ip is the 'source address'
- fault_va is the 'destination address'
- Temporary allow access to the data using MTF
- Redirect access to the fake data using MTF and EPT.PFN



^{*} AllMemPro details - http://bit.ly/AllMemPro

BLACK HAT SOUND BYTES OR CONCLUSION

Kernel-mode memory is out of control

 MemoryRanger isolates drivers execution by using a specific EPT structure for each driver

MemoryRanger seems to prevent Spectre and Meltdown CPU attacks: research is ongoing

Dīvide et Imperā* from Latin divide and rule

* Cartledge, P. (2013). Sparta and Lakonia: A regional history 1300-362 BC. Routledge.

Thank you!

Igor Korkin igor.korkin@gmail.com

All the details & my CV are here igorkorkin.blogspot.com

