

CastGuard

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Killing Bugs vs. Killing Exploit Techniques

Mitigating exploit techniques has ambiguous long-term value.

Mitigations are typically far enough away from actual bug that bugs are still exploitable using different techniques.

Tradeoffs between performance, compatibility, and mitigation durability are becoming increasingly difficult.

Unclear how many more practical opportunities there are for building meaningful exploit mitigations.

Killing Bugs vs. Killing Exploit Techniques

Microsoft increasingly focused on eliminating vulnerability classes, removing attack surface, and sandboxing code.

Hyper-V vPCI component refactored from a C kernel component to C++ (w/ GSL) user component.

Microsoft investigation of Rust and other safer systems languages, and use of managed languages.

CLFS blocked from sandboxes, Redirection Guard, etc.

Path Forward for C/C++ Code

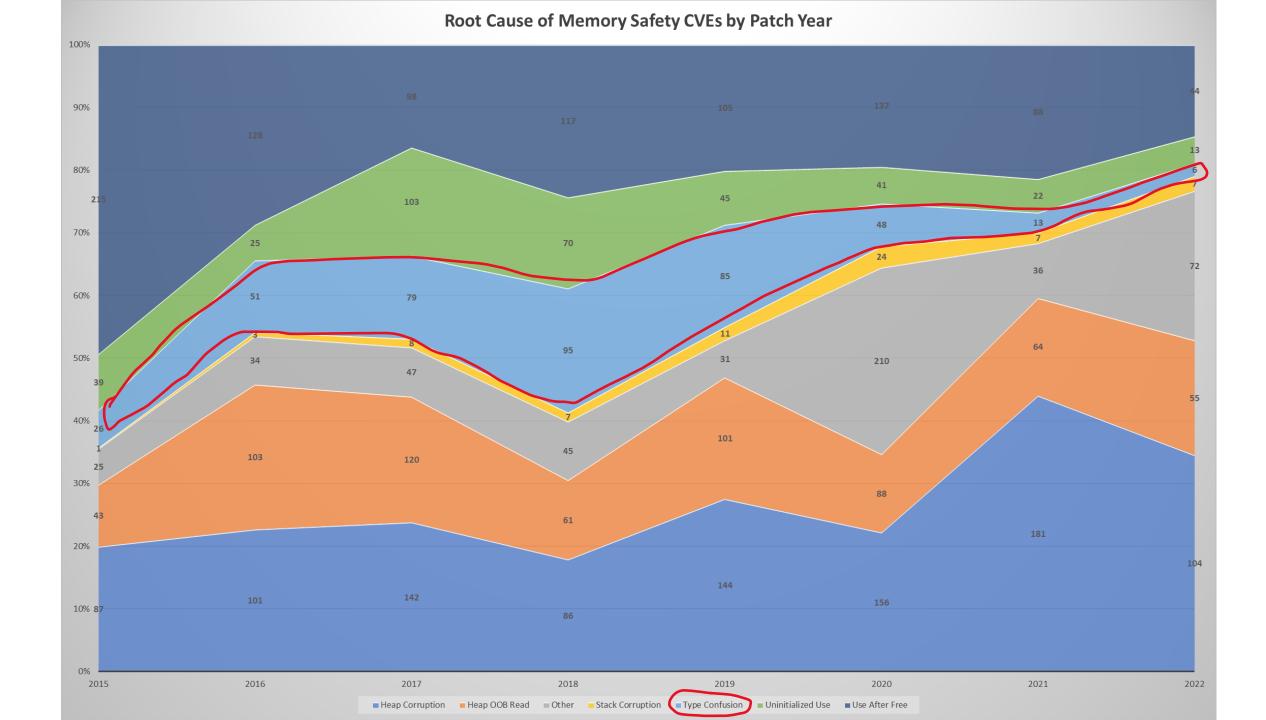
Four high-level bug classes responsible for majority of memory safety vulnerabilities.

Buffer Overflow & Out-of-Bounds Accesses (i.e. attacker controls array index)

Uninitialized Memory

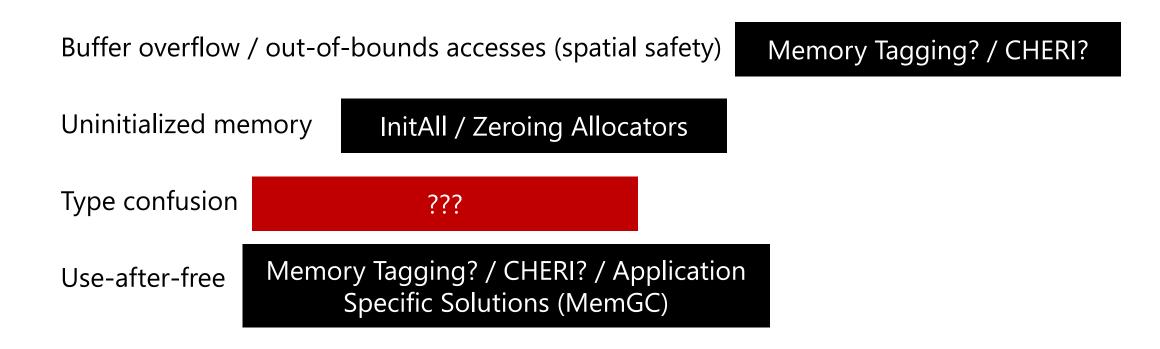
Type Confusion

Use-After-Free



Path Forward for C/C++ Code

Four high-level bug classes responsible for majority of memory safety vulnerabilities.



Not necessarily things Microsoft is committed to using, just illustrating solution space.

Type Confusion

Come in many flavors..

Illegal static downcast (down-casting to the wrong derived type in a class hierarchy).

Improper union use.

Illegal reinterpret_cast (i.e. cast an object of some type to totally different type).

Generic logic issues (i.e. using fields incorrectly).

Offer extremely powerful primitives to attackers and can often lead to breaking mitigations such as Memory Tagging.

Many forms of type confusion are not possible to generically solve .

Illegal Static Downcasts

```
struct Animal {
         virtual void WhoAmI() {cout << "Animal";}</pre>
};
struct Dog : public Animal {
         virtual void WhoAmI() {cout << "Dog";}</pre>
};
struct Cat : public Animal {
         virtual void WhoAmI() {cout << "Cat";}</pre>
Animal* myAnimal = new Dog();
static_cast<Cat>(myAnimal)->WhoAmI(); // Illegal down-cast
```

dynamic_cast?

Code creating the object and code casting object must enable Runtime Type Information (RTTI).

Makes it difficult to automatically convert existing static_cast's to dynamic_cast. Need to control all code to ensure RTTI settings are uniform. We cannot enforce this (3rd party DLL's, etc.).

RTTI causes binary size bloat (may be possible to optimize).

Windows.UI.Xaml.Controls.dll grows 86.5% from RTTI (no dynamic_cast).

dynamic_cast checks have overhead (may be possible to optimize).

Code executed in the success path of a dynamic_cast down-cast in a single-inheritance class hierarchy

| Code executed in the success path of a dynamic_cast down-cast in a single-inheritance class hierarchy | Code executed in the success path of a dynamic_cast down-cast in a single-inheritance class hierarchy | Code executed in the success path of a dynamic_cast down-cast in a single-inheritance class hierarchy | Code executed in the success path of a dynamic_cast down-cast in a single-inheritance class hierarchy | Code executed in the success path of a dynamic_cast down-cast in a single-inheritance class hierarchy | Code executed in the success path of a dynamic_cast down-cast in a single-inheritance class hierarchy | Code executed in the success path of a dynamic_cast down-cast in a single-inheritance class hierarchy | Code executed in the success path of a dynamic_cast down-cast in a single-inheritance class hierarchy | Code executed in the success path of a dynamic_cast down-cast in a single-inheritance class hierarchy | Code executed in the success path of a dynamic_cast down-cast dow

```
00007ff7`56771472 c744242000000000 mov
                                         dword ptr [rsp+20h], 0 ss:0000001f`baeff890=567bf330
00007ff7`5677147a 4c8d0ddf070600 lea
                                          r9, [test3!MyChild1 `RTTI Type Descriptor' (00007ff7 `567d1c60)]
00007ff7`56771481 4c8d0590080600 lea
                                         r8, [test3!MyBase `RTTI Type Descriptor' (00007ff7`567d1d18)]
00007ff7`56771488 33d2
                                         edx, edx
00007ff7`5677148a 488bcb
   test3!__RTDynamicCast:
00007ff7`567bd5b0 48895c2410
                                         qword ptr [rsp+10h], rbx ss:0000001f`baeff878=0000000
00007ff7`567bd5b5 4889742418
                                         qword ptr [rsp+18h], rsi
00007ff7`567bd5ba 57
                                 push
00007ff7`567bd5bb 4154
                                 push
00007ff7`567bd5bd 4155
                                 push
                                         r13
00007ff7`567bd5bf 4156
                                 push
                                        r14
00007ff7`567bd5c1 4157
                                         r15
00007ff7`567bd5c3 4883ec50
                                  sub
                                         rsp, 50h
00007ff7`567bd5c7 4d8bf9
                                  mov
                                         r15, r9
00007ff7`567bd5ca 4d8be0
                                         r12, r8
00007ff7`567bd5cd 4c63ea
                                  movsxd r13, edx
00007ff7`567hd5d0 488hf9
                                  mov
                                        rdi, rcx
00007ff7`567bd5d3 33db
                                         ebx, ebx
00007ff7`567bd5d5 4885c9
                                  test
                                         rcx, rcx
00007ff7`567bd5f9 488b70f8
                                          rsi, qword ptr [rax-8]
00007ff7`567bd5fd 8b4604
                                          eax, dword ptr [rsi+4
00007ff7`567bd600 4c8bf7
                                 mov
                                         r14, rdi
00007ff7`567bd603 4c2bf0
                                          edx, dword ptr [rsi+8]
00007ff7`567bd609 482bca
                                  sub
                                         rcx, rdx
00007ff7`567bd60c f7da
00007ff7`567bd60e 1bc0
                                          eax, eax
                                         eax, dword ptr [rcx]
00007ff7`567bd612 4863c8
                                  movsxd rcx, eax
00007ff7`567bd615 4c2bf1
                                        r14, rcx
00007ff7`567hd618 391e
                                         dword ptr [rsi], ebx
                                                                                         <-- take this jump
00007ff7`567hd63a 48634614
                                  movsxd rax, dword ptr [rsi+14h] ds:00007ff7\567c9b8c=00059b
00007ff7`567bd63e 4c8bce
00007ff7`567bd641 4c2bc8
                                          r9, rax
99997ff7`567bd644 48634619
                                  movsxd rax, dword ptr [rsi+10h
00007ff7`567bd648 428b4c0804
                                  mov ecx, dword ptr [rax+r9+4]
00007ff7`567bd64d f6c101
                                  test cl, 1
00007ff7`567bd652 4d8bc7
                                         r8, r15
00007ff7`567bd655 498bd4
                                         rdx, r12
00007ff7`567bd658 488bce
                                  mov
                                         rcx, rsi
```

```
| 09007ff7' 567bd669 482bca | sub | rcx, rdx | edw | edx | e
```

```
test3|FindSTTargetTypeTnstance:
00007ff7`567bd11c 488bc4
00007ff7`567bd11f 48895808
                                     qword ptr [rax+8], rbx
00007ff7`567bd123 48896810
                                     gword ptr [rax+10h], rbp
00007ff7`567bd127 48897018
                                     qword ptr [rax+18h], rsi
00007ff7`567bd12b 48897820
                                     qword ptr [rax+20h], rdi
 00007ff7`567bd12f 4156
                              movsxd rax, dword ptr [rcx+10h
00007ff7`567bd135 498bd8
                              mov
                                     rbx, r8
00007ff7`567bd138 33c9
                                     ecx, ecx
00007ff7`567hd13a 4c8hf2
                                     r14, rdx
                              movsxd r11, dword ptr [rax+
  007ff7`567bd142_468b540808
                                      r10d, dword ptr [rax+r9+8]
00007ff7`567bd147 4d03d9
                              add
                                     r11, r9
00007ff7`567bd14a 4585d2
                                     r10d, r10d
00007ff7`567hd14f 4d8hc3
                             mov r8, r11
00007ff7`567bd155 4903d1
                                     rdx, r9
                              add
00007ff7`567bd158 ffc1
                              inc
                              movsxd rax, dword ptr [rdx]
00007ff7`567bd15d 4903c1
                              add
                                     rax. r9
00007ff7`567hd160 483hc3
00007ff7`567bd1cc 413bca
                             cmp
                                     ecx, r10d
00007ff7`567bd1d1 4d8d048b
                              lea
                                     r8, [r11+rcx*4]
00007ff7`567bd1d5 496300
                              movsxd rax, dword ptr [r8]
00007ff7 567bd1d8 42f644081404 test byte ptr [rax+r9+14h], 4
                              movsxd rax, dword ptr [rax+r9]
00007ff7`567bd1e4 4903c1
                              add
                                   rax. r9
00007ff7`567hd1e7 493hc6
                              cmp
00007ff7`567bd1f9 488bc2
                             mov rax, rdx
 0007ff7`567bd1ba 488b6c2418
                                      rbp, qword ptr [rsp+18
 0007ff7`567bd1bf 488b742420 mov
                                     rsi, qword ptr [rsp+20
 0007ff7`567bd1c4 488b7c2428 mov
                                     rdi, qword ptr [rsp+28h
 0007ff7`567bd1c9 415e
 00007ff7`567bd1cb c3
; back to test3!__RTDynamicCast
                                  jmp test3! RTDynamicCast+0xdf (00007ff7`567bd68f) <-- ju</pre>
00007ff7`567bd68f 4c8bc8
                                        r9, rax
00007ff7`567bd692 4885c0
                                         rax, rax
                                movsxd rax, dword ptr [r9+8] ds:00007ff7\S67c9bd
00007ff7`567bd6c4 4803c3
00007ff7`567bd6c7 4903c6
                                       rax, r14
00007ff7`567bd5dc 4c8d5c2450
                                  lea r11, [rsp+50h]
  0007ff7`567bd5e1 498b5b38
                                         rbx, qword ptr [r11+38
                                         rsi, qword ptr [r11+40h
00007ff7`567hd5o0 408ho3
                                 mov rsp, r11
 0007ff7`567bd5ee 415e
 00007ff7`567bd5f0 415d
 00007ff7`567bd5f2 415c
 0007ff7`567bd5f4 5f
 00007ff7`567bd5f5_c3
```

: dynamic cast check over



Concept

To protect against illegal downcast, object needs a type identified that can be checked.

We cannot change object layout or we break the world.

Objects with a vftable already have an identifier, the vftable.

Automatically convert all static_cast on classes with vftables in to CastGuard protected casts.

Terminology

```
void Foo (Animal* animal) {
    static_cast<Cat*>(animal);
}
```

LHS Type: Left-hand side type being cast to (Cat*).

RHS Type: The statically declared right-hand side type (Animal*).

Underlying Type: The actual type of the RHS object (unknown at compile time).

Threat Model / Requirements

Code must be compiled using Link Time Code Generation (LTCG).

Code creating the object in the same LTCG module as code casting the object.

LHS type and RHS type have at least 1 vftable.

Object is valid (i.e. if RHS type is Animal*, it is a valid Animal*).

If some other component already illegally casted the object, we will not provide protection.

First-order memory safety vulnerability is the type confusion (i.e. attacker doesn't already have memory corruption).



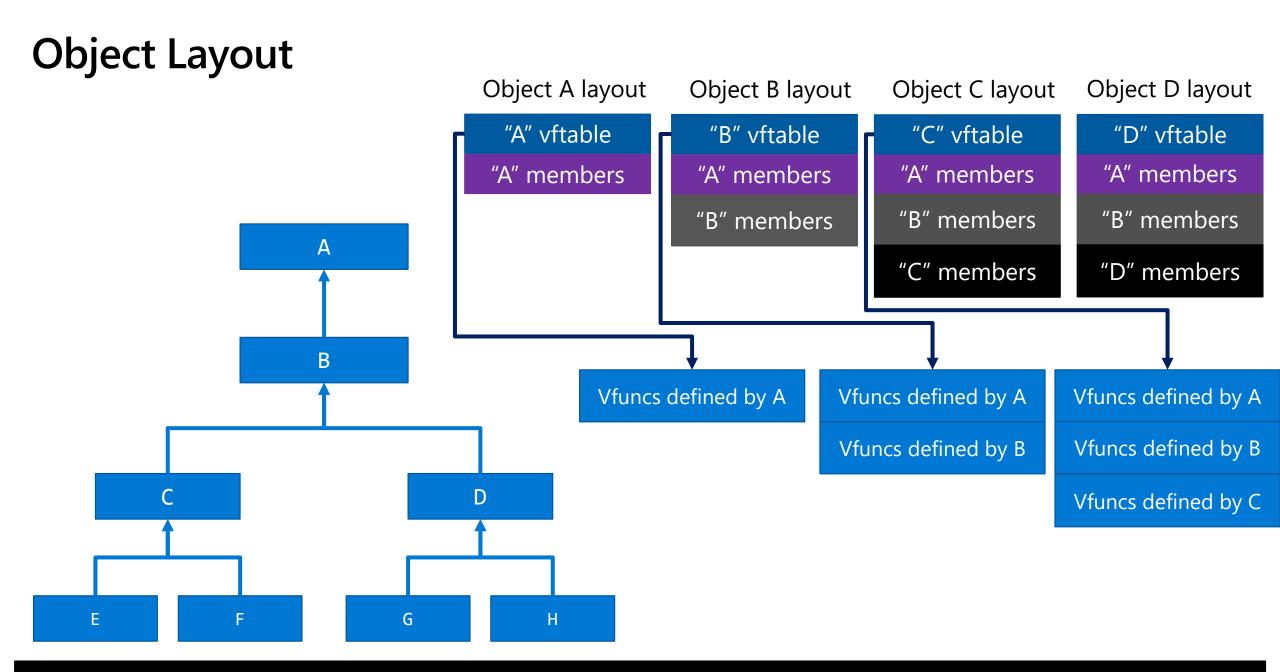
What The Compiler Knows

```
void Foo (Animal* animal) {
    static_cast<Cat*>(animal);
}
```

This is a static downcast from Animal* to Cat*.

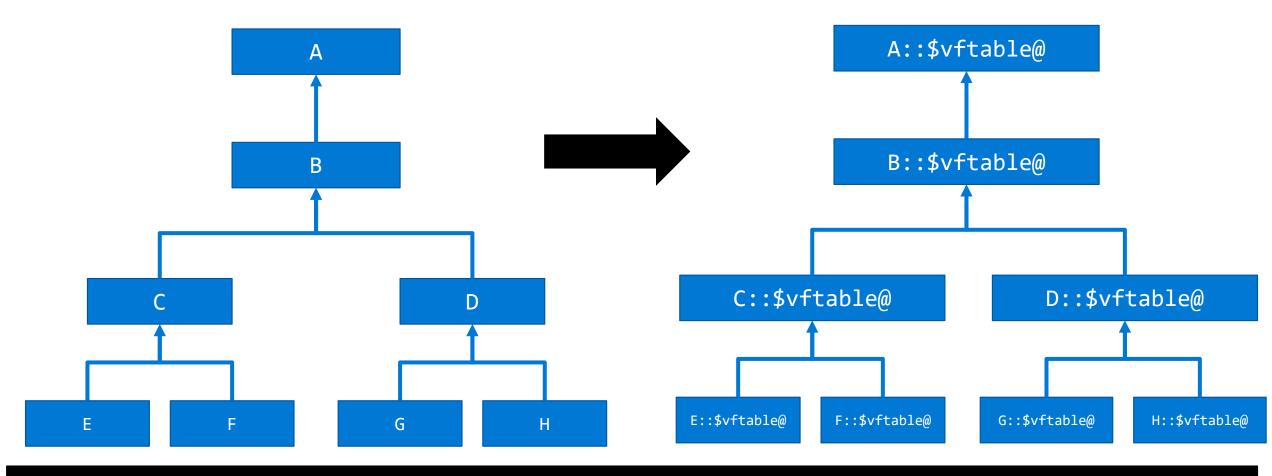
The offset into Cat* and Animal* that the vftable pointer is located.

The location (RVA into binary) of the vftables for every type in this hierarchy (i.e. where the vftables are laid out in the binary).



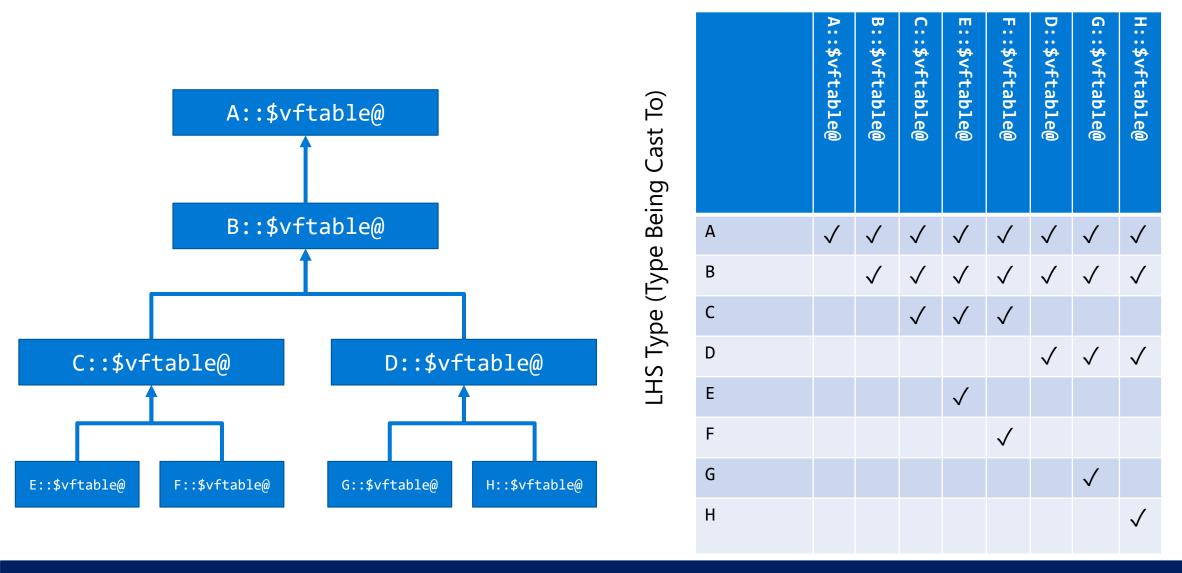
Derived types begin with their base types layout and append their own member variables after the base type.

The Vftable View of the World



It is helpful to think about class hierarchies in terms of the vftables as that is the unique identifier

Legal Underlying Vftables



This table shows the vftables are that legal for a pointer of some specific type to have

Naïve Check (To Understand Concepts)

User Code:

```
void MyFunction (B* b)
{
    static_cast<C*>(b);
}
```

Compiler Inserts:

```
// Ensure the vftable is one of the legal
// vftables, if not, fast-fail
if (b != NULL)
    if (b->vftable != C::$vftable@ &&
       b->vftable != E::$vftable@ &&
       b->vftable != F::$vftable@)
       fast-fail
```

Naïve Check (To Understand Concepts)

```
void MyFunction (B* b)
{
    static_cast<C*>(b);
}
```

Compiler Inserts CodeGen:

```
// Ensure the vftable is one of the legal
// vftables, if not, fast-fail
if (b != NULL)
{
    if (b->vftable != C::$vftable@ &&
        b->vftable != E::$vftable@ &&
        b->vftable != F::$vftable@)
    {
        fast-fail
    }
}
```

Optimization Step 1: Lay Out Vftables Together in Binary

To make the example simple, assume 64-bit architecture, each vftable has a single virtual function, and no RTTI information. Total size per vftable is 8 bytes.

We'll talk about the global variables __CastGuardVftableStart and __CastGuardVftableEnd later.

CastGuard Vftable Region

| Offset | Name |
|--------|-----------------------|
| 0x00 | CastGuardVftableStart |
| 0x08 | A::\$vftable@ |
| 0x10 | B::\$vftable@ |
| 0x18 | C::\$vftable@ |
| 0x20 | D::\$vftable@ |
| 0x28 | E::\$vftable@ |
| 0x30 | F::\$vftable@ |
| 0x38 | G::\$vftable@ |
| 0x40 | H::\$vftable@ |
| 0x48 | CastGuardVftableEnd |

Vftables CastGuard cares about are laid out in their own contiguous region.

| Offset | Name |
|--------|-----------------------|
| 0x00 | CastGuardVftableStart |
| 0x08 | A::\$vftable@ |
| 0x10 | B::\$vftable@ |
| 0x18 | C::\$vftable@ |
| 0x20 | D::\$vftable@ |
| 0x28 | E::\$vftable@ |
| 0x30 | F::\$vftable@ |
| 0x38 | G::\$vftable@ |
| 0x40 | H::\$vftable@ |
| 0x48 | CastGuardVftableEnd |

Legal Underlying Vftables

| LHS Type (Type being Cast 10) | | \::\$vftable@ | 3::\$vftable@ | C::\$vftable@ | :::\$vftable@ | :::\$vftable@ |)::\$vftable@ | 3::\$vftable@ | 1::\$∨†tab⊥e@ |
|-------------------------------|---|---------------|---------------|---------------|---------------|---------------|---------------|---------------|---------------|
| e De | Α | \checkmark | \checkmark | \checkmark | ✓ | \checkmark | \checkmark | \checkmark | √ |
|) D | В | | \checkmark | \checkmark | \checkmark | \checkmark | \checkmark | \checkmark | √ |
|) ed | С | | | \checkmark | ✓ | ✓ | | | |
| <u>></u> | D | | | | | | \checkmark | \checkmark | √ |
| Ĭ | Е | | | | √ | | | | |
| | F | | | | | √ | | | |
| | G | | | | | | | √ | |
| | Н | | | | | | | | √ |
| | | | | | | | | | |

Create a bitmap per LHS Type being cast to which indicates which vftables are legal for that cast

| Offset | Name |
|--------|-----------------------|
| 0x00 | CastGuardVftableStart |
| 0x08 | A::\$vftable@ |
| 0x10 | B::\$vftable@ |
| 0x18 | C::\$vftable@ |
| 0x20 | D::\$vftable@ |
| 0x28 | E::\$vftable@ |
| 0x30 | F::\$vftable@ |
| 0x38 | G::\$vftable@ |
| 0x40 | H::\$vftable@ |
| 0x48 | CastGuardVftableEnd |

How to Create Bitmap:

Each type being downcast to gets its own bitmap (i.e. B, C, D, etc.).

For each bitmap:

- 1. Choose a "base vftable". This is the vftable you will compare the underlying types vftable against. It should be the first vftable (lowest RVA) in the binary that is legal for the cast.
- 2. Compute the offset between this vftable and all other vftables that are legal for the cast.
- 3. Each legal vftable is "1" in the bitmap. Illegal vftables are "0".

For cast to "C", "base vftable" is C::\$vftable@

| Offset | Name | |
|--------|-----------------------|---|
| 0x00 | CastGuardVftableStart | |
| 0x08 | A::\$vftable@ | |
| 0x10 | B::\$vftable@ | |
| 0x18 | C::\$vftable@ | 4 |
| 0x20 | D::\$vftable@ | 4 |
| 0x28 | E::\$vftable@ | 4 |
| 0x30 | F::\$vftable@ | 4 |
| 0x38 | G::\$vftable@ | |
| 0x40 | H::\$vftable@ | |
| 0x48 | CastGuardVftableEnd | |

```
Offset from Ox0 Ox8 Ox10 Ox18 C::$vftable@ 1 O 1 1
```

How To Use Bitmap:

```
delta = Object->Vftable - C::$vftable@
ordinal = delta ROR 3 // shift out low 3 bits
```

C_Bitmap[ordinal] == 1 if cast allowed

| Offset | Name |
|--------|-----------------------|
| 0x00 | CastGuardVftableStart |
| 0x08 | A::\$vftable@ |
| 0x10 | B::\$vftable@ |
| 0x18 | C::\$vftable@ |
| 0x20 | D::\$vftable@ |
| 0x28 | E::\$vftable@ |
| 0x30 | F::\$vftable@ |
| 0x38 | G::\$vftable@ |
| 0x40 | H::\$vftable@ |
| 0x48 | CastGuardVftableEnd |

Offset of underlying type vftable from "base vftable"

| | | 0x0 | 0x8 | 0x10 | 0x18 | 0x20 | 0x28 | 0x30 | 0x38 |
|-------|----------|-----|-----|------|------|------|------|------|------|
| ט | B_Bitmap | 1 | 1 | 1 | 1 | 1 | 1 | 1 | |
| ıype | C_Bitmap | 1 | 0 | 1 | 1 | | | | |
| | D_Bitmap | 1 | 0 | 0 | 1 | 1 | | | |
| _ | E_Bitmap | 1 | | | | | | | |
| | F_Bitmap | 1 | | | | | | | |
| ышар | G_Bitmap | 1 | | | | | | | |
| | H_Bitmap | 1 | | | | | | | |

Only create bitmap for types that are down-cast to. Minimizes binary size.

Bitmap alignment can change to reduce binary size. This example uses 8-byte alignment (the minimum on 64-bit) but we may increase the alignment of vftables to reduce the size of the bitmap.

Better Check

```
void MyFunction (B* b)
{
    static_cast<C*>(b);
}
```

Compiler Inserts CodeGen:

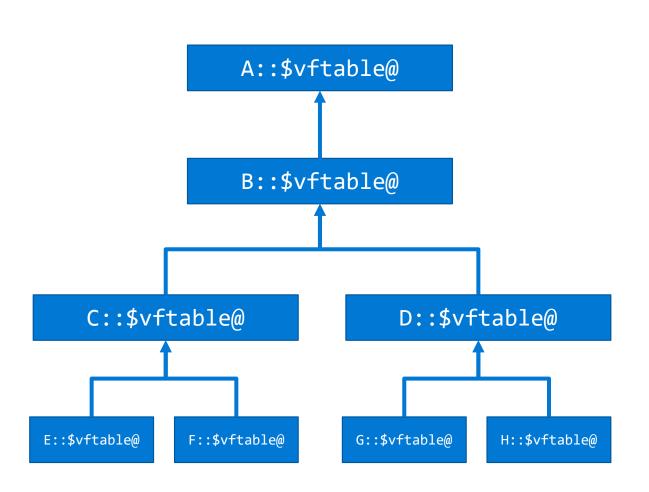
```
if (b != null) {
    // read the vftable from the object
    uint64 ptr = b->vftable;
    // get offset from the first valid vftable for this cast
    uint64 delta = ptr - &C::$vftable@
    // vftables are 8 byte aligned
    // if any low 3 bits are set, ROR will shift them to high bits
    uint64 ordinal = delta ROR 3;
    // test the bitmap to see if this is valid
    if (ordinal >= sizeof_in_bits(C_Bitmap))
        !bittest(C Bitmap, ordinal))
        fast-fail
```

More Optimization

· With a few realizations, we can do much better than this.

- · Bitmaps are not ideal because:
 - · It takes a memory load to consult them.
 - They take up space in the binary.

Order the Vftables Depth First



CastGuard Vftable Region

| Name |
|-----------------------|
| CastGuardVftableStart |
| A::\$vftable@ |
| B::\$vftable@ |
| C::\$vftable@ |
| E::\$vftable@ |
| F::\$vftable@ |
| D::\$vftable@ |
| G::\$vftable@ |
| H::\$vftable@ |
| CastGuardVftableEnd |
| |

Create Bitmaps

| Offset | Name |
|--------|-----------------------|
| 0x00 | CastGuardVftableStart |
| 0x08 | A::\$vftable@ |
| 0x10 | B::\$vftable@ |
| 0x18 | C::\$vftable@ |
| 0x20 | E::\$vftable@ |
| 0x28 | F::\$vftable@ |
| 0x30 | D::\$vftable@ |
| 0x38 | G::\$vftable@ |
| 0x40 | H::\$vftable@ |
| 0x48 | CastGuardVftableEnd |

Bitmap for LHS Type

Offset of underlying type vftable from LHS Type vftable

| | 0x0 | 0x8 | 0x10 | 0x18 | 0x20 | 0x28 | 0x30 | 0x38 |
|----------|-----|-----|------|------|------|------|------|------|
| B_Bitmap | 1 | 1 | 1 | 1 | 1 | 1 | 1 | |
| C_Bitmap | 1 | 1 | 1 | | | | | |
| D_Bitmap | 1 | 1 | 1 | | | | | |
| E_Bitmap | 1 | | | | | | | |
| F_Bitmap | 1 | | | | | | | |
| G_Bitmap | 1 | | | | | | | |
| H_Bitmap | 1 | | | | | | | |

Property: When ordered with a DFS, legal vftables are always laid out contiguously (thus you never see 0's in the bitmap)

Range Check

| Offset | Name |
|--------|-----------------------|
| 0x00 | CastGuardVftableStart |
| 0x08 | A::\$vftable@ |
| 0x10 | B::\$vftable@ |
| 0x18 | C::\$vftable@ |
| 0x20 | E::\$vftable@ |
| 0x28 | F::\$vftable@ |
| 0x30 | D::\$vftable@ |
| 0x38 | G::\$vftable@ |
| 0x40 | H::\$vftable@ |
| 0x48 | CastGuardVftableEnd |

- If bitmap is all ones, no need to check the bitmap. As long as the ordinal is in bounds of the bitmap you succeed.
- Taking it further: Rather than shifting the pointer to calculate the ordinal, just do a range check.

If vftable base address is within 0x10 bytes of C::\$vftable&, this object is a valid "C"

Range Check

```
void MyFunction (B* b)
{
    static_cast<C*>(b);
}
```

Compiler Inserts CodeGen:

```
if (b != null) {
    // read the vftable from the object
    uint64 ptr = b->vftable;
    // get offset from the first valid vftable for this cast
    uint64 offset = ptr - &C::'vftable'
    // can be C, E, or F
    // vftable expected to be 0x0, 0x8, or 0x10 bytes
    // offset from C::$vftable@
    if (offset > 0x10) {
        fast-fail
```

Concerns?

What if the vftable is offset 0x9? That would pass the check but is illegal!

Not a concern due to threat model, there is no way a legitimate object could be created with that vftable pointer. We are assuming the first order vulnerability is this static_cast so the object must be well formed.

Compiler Inserts CodeGen:

```
if (b != null) {
    // read the vftable from the object
    uint64 ptr = b->vftable;
    // get offset from the first valid vftable for this cast
    uint64 offset = ptr - &C::'vftable'
    // can be C, E, or F
    // vftable expected to be 0x0, 0x8, or 0x10 bytes
    // offset from C::$vftable@
    if (offset > 0x10) {
        fast-fail
```

Compatibility

What if the object was created in a different DLL?

The LTCG compiler pass will not know about these vftables. The cast might be legitimate but because the vftable comes from a different DLL it isn't laid out where CastGuard expects.

What if the object was created in a static library?

Mostly similar concern, with caveats. See appendix for more details.

Modified Check for Compatibility

```
if (ptr != null) {
    // get offset from the first valid vftable for
    // this cast
    uint64 offset = ptr - &C::$vftable@
    // can be C, E, or F
    // vftable expected to be 0x0, 0x8, or 0x10 bytes
    // offset from C::$vftable@
    if (offset > 0x10 &&
        ptr > CastGuardVftableStart &&
                                                    Only fast-fail if the underlying vftable is being tracked by
        ptr < CastGuardVftableEnd) {</pre>
                                                       CastGuard, otherwise "fail open" for compatibility
        fast-fail
```

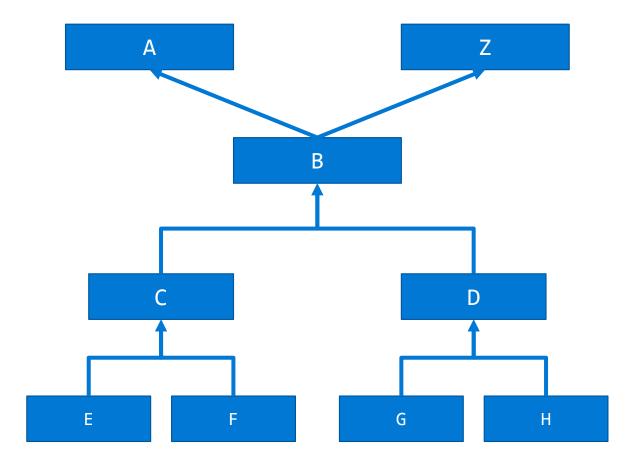
AMD64 Assembly

```
; rcx == The right-hand side object pointer.
; First do the nullptr check. This could be optimized away but is not today.
; N.B. If the static cast has to adjust the pointer base, this nullptr check
; already exists.
4885c9
              test
                      rcx, rcx
7416
                       codegentest!DoCast+0x26
               ie
; Next load the RHS vftable and the comparison vftable.
488b11
                       rdx, qword ptr [rcx]
               mov
                      r8, [codegentest!C::`vftable']
4c8d05ce8f0500 lea
; Now do the range check. Jump to the AppCompat check if the range check fails.
492bc0
               sub
                       rdx, r8
4883f820
                       rdx, 20h
               cmp
7715
                       codegentest!DoCast+0x3b
                                                    ; Jump to app-compat check
               ja
```

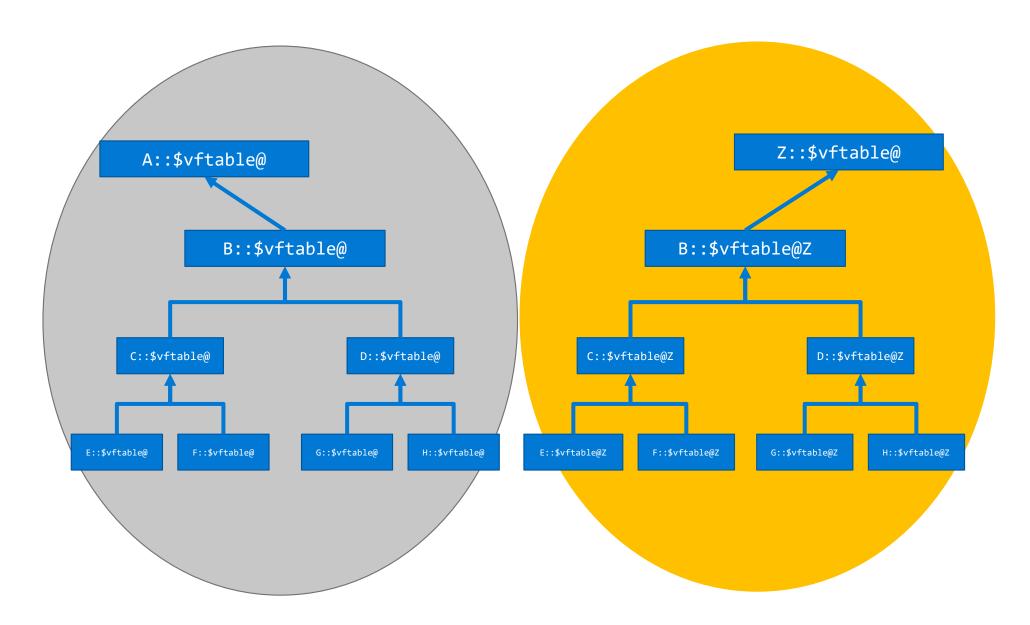
Multiple Inheritance

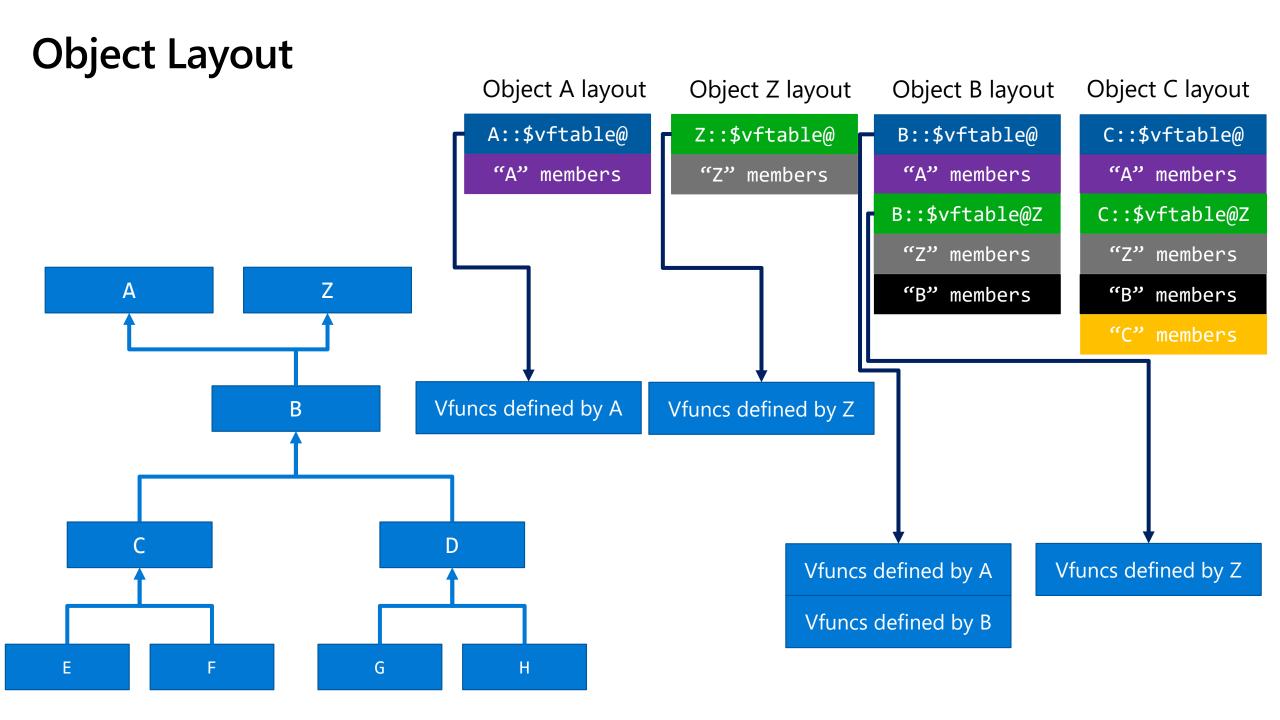


Multiple Inheritance Example



Vftable View





Which Vftable to Use

Depends on what the current RHS type is.

```
If RHS == "A", need to use vftable that "A" introduced.
```

If RHS == "Z", need to use vftable that "Z" introduced.

Otherwise, can use either. Prefer the vftable that is closest to the base address of the object to reduce code size.

If vftable is at offset 0 in object, the "this" pointer doesn't need to be adjusted.

Notes

Once you realize there are multiple vftable hierarchies, multiple inheritance becomes identical to single inheritance.

Choose the vftable hierarchy to do checks against based on the RHS type.

Lay out vftable hierarchy using depth-first layout.

Do a simple range check on the vftable.

Virtual Base Hierarchies



Virtual Base

See appendix for full information (not enough time).

A nasty and rarely used C++ feature that allows "more efficiently" doing multiple inheritance when both parent classes inherit from the same base class.

Can make range checks impossible for a vftable hierarchy, need to use bitmap checks.



Misc.

Identical Comdat Folding (ICF) must be disabled for vftables in the "CastGuard region".

ICF will eliminate duplicate copies of data (i.e. vftables that are identical) but CastGuard requires all vftables are unique since they are used as identifiers.

OptRef (remove unreferenced symbols) linker optimization also disabled for CastGuard vftables.

If the linker deletes an unreferenced vftable in the CastGuard region, it changes the layout of the region but we already generated code based on the expected layout.

Luckily LTCG does a pretty good job up-front at determining if a symbol is unreferenced and we won't lay out the symbol in the first place.



```
struct A
    A::A(){}
    virtual void Entry()
        return;
};
template <class T>
struct B : A
    virtual void Entry()
        static cast<T*>(this)->WhoAmI();
    void DoStuff()
        DoOtherStuff(static cast<T*>(this));
```

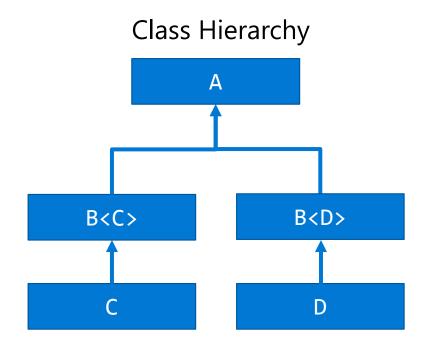
An object of type "B" is never created. Only derived types are created.

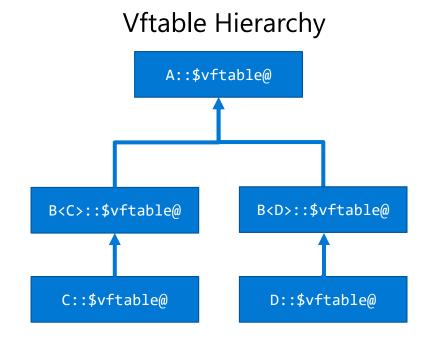
Derived type specifies itself as a templated parameter when inheriting from "B" class.

```
struct C : B<C>
{
    void WhoAmI()
    {
        PrintWhoIAm();
    }

    static void PrintWhoIAm()
    {
        printf("C");
    }
};
struct D : B<D>
    {
        void WhoAmI()
        {
            PrintWhoIAm();
        }
        printf("C");
        }
};
```

Because "B" is specialized based on the type inheriting from it, any given "B" type (such as B<C>) only has a single derived type.





```
struct A
{
    A::A(){}

    virtual void Entry()
    {
        return;
    }
};
```

```
template <class T>
struct B : A
{
    virtual void Entry()
    {
        static_cast<T*>(this)->WhoAmI();
    }

    void DoStuff()
    {
        DoOtherStuff(static_cast<T*>(this));
    }
};
```

"DoStuff" for B<C> and B<D> are identical. Linker will de-duplicate them.

```
struct C : B<C>
{
    void WhoAmI()
    {
        PrintWhoIAm();
    }

    static void PrintWhoIAm()
    {
        printf("C");
    }
};
```

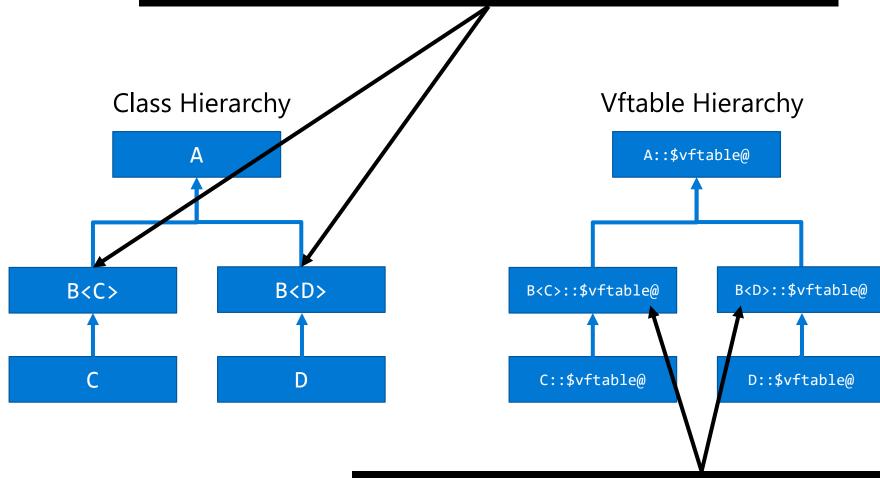
```
struct D : B<D>
{
    void WhoAmI()
    {
        PrintWhoIAm();
    }

    static void PrintWhoIAm()
    {
        printf("D");
    }
};
```

Vftable for C & B<C>, D & B<D> are identical. Linker will de-duplicate.

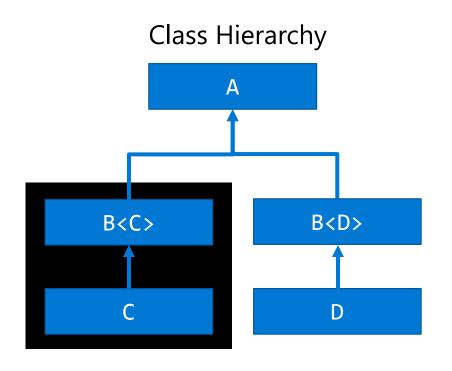
CastGuard checks make "DoStuff" implementations different (type specific cast checks) and reference vftables so they can no longer be de-duplicated. Some WinRT binaries got ~20% bigger.

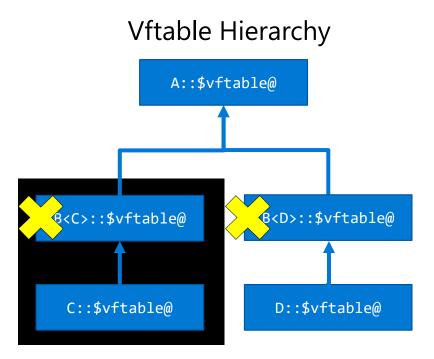
These types are never created, so a valid object will NEVER have their vftables.



No reason to include these vftables in checks since an object can never have them.

```
void Demo(B<C>* MyPtr) {
    static_cast<C*>(MyPtr);
}
```





A "B<C>*" either points to a "B<C>" or a "C". We know "B<C>" was never created, must be "C".

The only vftable a "B<C>*" pointer could have is "C::\$vftable@" which is legal when casting to "C", no point in doing a cast check.

Optimizing for CRTP

Optimization can also help non-CRTP related casts.

We were able to statically prove away EVERY cast check in Windows.UI.Xaml.Controls.dll, going from 20% binary size regression to 0%.

Similar CRTP optimization issues exist for other technologies.

XFG (Extended Flow Guard) – Caused a 43% binary size regression on "Windows.UI.Xaml.Controls.dll" due to fine-grain indirect call signature checks. We got this fixed by making function signature ignore template specializations.

Clang CFI – Breaks CRTP code sharing due to fine-grain indirect call signature checks and cast checks (when derived-cast checks enabled). Likely a similar regression to CastGuard and XFG.

Performance

Near-zero runtime overhead

- · Spec 2006 showed no regression.
- No overhead detected in Windows components.

Binary size impact under 1%

- · Components without downcasting have no overhead.
- CRTP optimization can statically prove safety of many casts.

Future Possibilities

Strict Mode

- Mark hierarchies as "strict" indicating they should never have an app compat check, all failures are fatal.
- Could force full bitmap checks (defend against type confusions caused by memory corruption).

Acceleration for dynamic_cast

 Dynamic_cast hot path uses CastGuard style check, only does the full check in the app compat check path.

Just ideas – nothing committed.

Conclusion

It is possible to provide performant cast checks to prevent certain types of type confusion. May even be possible to use CastGuard tech in the hot-path of dynamic_cast.

CastGuard is flighting in Hyper-V code in Windows Insider Preview builds.

Additional Windows components will use CastGuard in the future.

Acknowledgements

CastGuard would not be possible without:

- Inspiration from Clang/LLVM –fsanitize=cfi-derivedcast.
- Many folks across Windows, Visual Studio, and MSRC.

Appendix

Virtual Base Inheritance



Overview

Inheritance works efficiently because offsets can be computed statically.

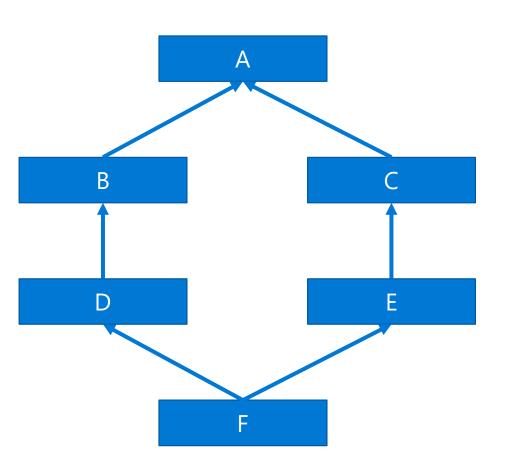
A pointer to an object can be upcast or downcast using simple pointer arithmetic based on where the type being casted to is "laid out" in the object's memory.

The same is generally true for multiple inheritance.

Object layout is known at compile time and any static_cast can be statically computed at compile time.

Things get complicated when the same base class is inherited from multiple times (i.e. diamond pattern).

Non-Virtual Base Diamond Pattern



Object "F" layout:

```
0x0 vftable of A
0x8 <members of A>
0x? <members of B>
0x? <members of D>
0x? vftable of A
0x? <members of A>
0x? <members of C>
0x? <members of E>
0x? <members of F>
```

The object "A" is inherited from twice and so there are two copies of it in "F". This may not be desirable.

Note: Cast checks for this pattern are identical to single inheritance / multiple inheritance

Diamond Pattern Issues

An object of type "F" contains two copies of the type "A".

"A" is inherited from by both "B" and "C". Which means "B" and "C" have their own copy of "A".

C++ does not automatically determine "you are using multiple inheritance and inheriting from the same base class twice so I will de-duplicate the object". Sometimes having multiple copies of the same base object is actually desired.

Can lead to bizarre behavior.

You cannot directly static_cast a pointer of type "A" to a pointer of type "F". "F" has two copies of "A" so you need to first static_cast to either "B" or "C" and then cast to "F" so the compiler knows which copy of "A" you are trying to access.

Virtual Base Inheritance Offers a Solution

Allows the compiler to de-duplicate the base class "A".

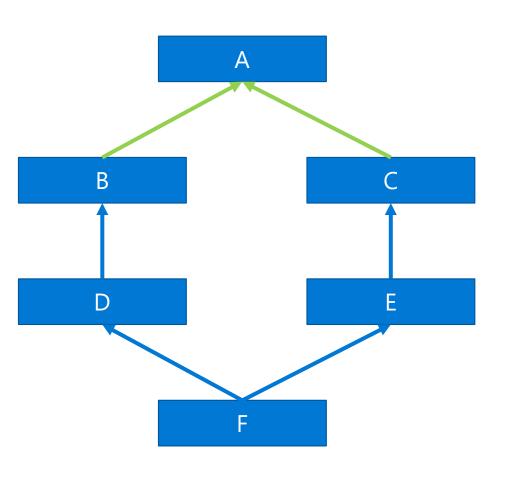
Anything that inherits from "A" cannot trivially cast to it since the location of "A" depends on the layout of the underlying type that was created.

i.e. an object of type "B" and type "F" may have "A" laid out at different positions.

A virtual base table is created in each object that contains the offset of "A" from the current "this" pointer.

Casting to "A" requires looking up how to adjust the current "this" pointer by reading from the virtual base table.

Virtual Base Diamond Pattern



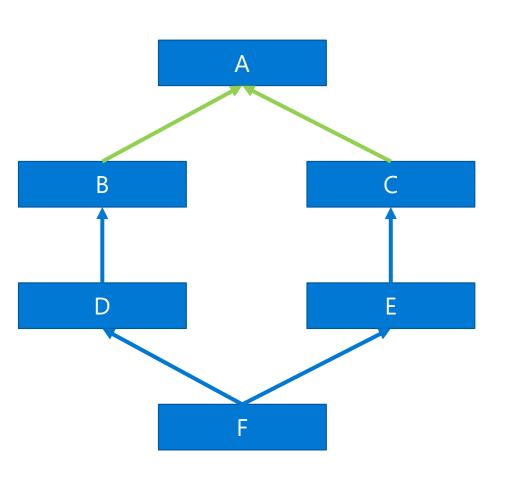
Object "F" layout:

```
<vbase table of B>
0x0
0x?
     <members of B>
    <members of D>
0x3
0x?
    vftable of A
    <members of A>
0x?
0x?
     <vbase table of C>
0x?
    <members of C>
    <members of E>
0x3
0x?
    <members of F>
```

There is a single copy of "A" in the object "F"

The object has a "virtual base table" to identify where "A" is relative to the object base.

Virtual Base Diamond Pattern



Virtual base inheritance allows for a single copy of the A.

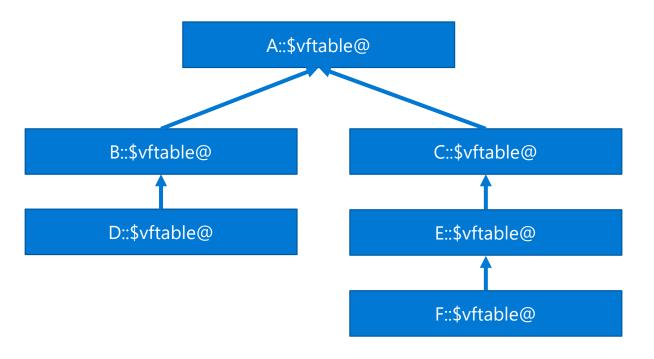
Virtual base table contains the offset from the "this" pointer of the current object to the base of "Object A" inside the object.

Object "F" layout:

| 0x0 | <pre><vbase b="" of="" table=""></vbase></pre> |
|-----|--|
| 0x? | <members b="" of=""></members> |
| 0x? | <members d="" of=""></members> |
| 0x? | <pre><vbase c="" of="" table=""></vbase></pre> |
| 0x? | <members c="" of=""></members> |
| 0x? | <members e="" of=""></members> |
| 0x? | <members f="" of=""></members> |
| 0x? | vftable of A |
| 0x? | <pre><members a="" of=""></members></pre> |

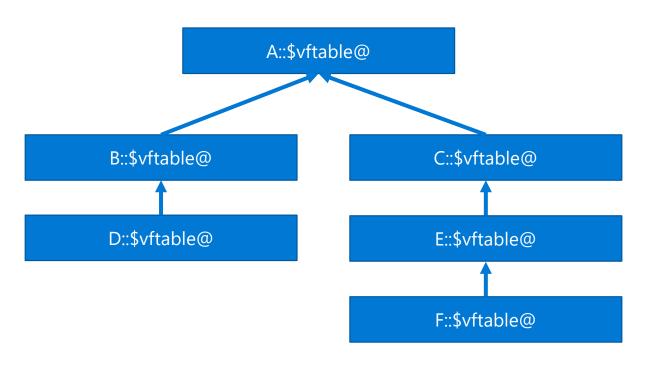
Object "B" layout:

Virtual Base Vftable View



| Offset | Name |
|--------|-----------------------|
| 0x00 | CastGuardVftableStart |
| 80x0 | A::\$vftable@ |
| 0x10 | B::\$vftable@ |
| 0x18 | D::\$vftable@ |
| 0x20 | C::\$vftable@ |
| 0x28 | E::\$vftable@ |
| 0x30 | F::\$vftable@ |
| 0x38 | CastGuardVftableEnd |

Cast Validity



Legal Underlying Vftables

| | A::\$vftable@ | B::\$vftable@ | D::\$vftable@ | C::\$vftable@ | E::\$vftable@ | F::\$vftable@ |
|---|---------------|---------------|---------------|---------------|---------------|---------------|
| Α | \checkmark | \checkmark | \checkmark | \checkmark | \checkmark | \checkmark |
| В | | \checkmark | \checkmark | | | \checkmark |
| С | | | | √ | √ | √ |
| D | | | ✓ | | | √ |
| Е | | | | | √ | √ |
| F | | | | | | \checkmark |

LHS Type (Type Being Cast To)

Bitmap Creation

| Bitmap bits relative to start of object | | | | | | | | | |
|---|---|---|---|---|---|---|--|--|--|
| A_Bitmap | 1 | 1 | 1 | 1 | 1 | 1 | | | |
| B_Bitmap | 1 | 1 | 0 | 0 | 1 | | | | |
| C_Bitmap | 1 | 1 | 1 | | | | | | |
| D_Bitmap | 1 | 0 | 0 | 1 | | | | | |
| E_Bitmap | 1 | 1 | | | | | | | |
| F_Bitmap | 1 | | | | | | | | |

Legal Underlying Vftables

| | A::\$vftable@ | B::\$vftable@ | D::\$vftable@ | C::\$vftable@ | E::\$vftable@ | F::\$vftable@ |
|---|---------------|---------------|---------------|---------------|---------------|---------------|
| Α | √ | √ | √ | √ | √ | √ |
| В | | \checkmark | \checkmark | | | \checkmark |
| С | | | | \checkmark | \checkmark | √ |
| D | | | ✓ | | | √ |
| Е | | | | | ✓ | √ |
| F | | | | | | ✓ |

LHS Type (Type Being Cast To)

Notes

- · Bitmap has zeros in it, cannot be optimized to a range check.
- · With complicated virtual base hierarchies, there may be no way to order the vftables that results in bitmaps of all 1's.
 - · We don't even attempt to brute force alternate vftable orderings since very little code uses virtual base, just pay the overhead.

```
; Start of CastGuard check
; rcx == the right-hand side object pointer.
; First do the nullptr check. This could be optimized away but is not today.
; N.B. If the static cast has to adjust the pointer base, this nullptr check
; already exists.
4885c9
               test
                       rcx, rcx
7433
               jе
                       CodeGenTest!DoCast+0x3e
; Load the virtual base table
488b01
                       rax, qword ptr [rcx]
               mov
; Right-hand side pointer adjustment (not part of CastGuard)
488d59e8
               lea
                       rbx, [rcx-18h]
; Read from virtual-base table
8b5004
                       edx, dword ptr [rax+4]
               mov
                                                                           ; Do the ordinal check using an ROL instruction to force alignment
; Load vftable to compare against
                                                                           ; Low bits below the alignment get shifted to high bits making the
488d057c6c0500 lea
                       rax, [CodeGenTest!MyGrandChild1::`vftable']
                                                                           ; value huge.
                                                                           482bd0
                                                                                                  rdx, rax
                                                                                          sub
; Add the offset read from the virtual-base table to the object pointer
                                                                           48c1c240
                                                                                          rol
                                                                                                  rdx, 3dh
                                                                                                             ; shift out low 3 bits
4803ca
               add
                       rcx, rdx
                                                                                                              ; ordinal range check
                                                                           4883fa01
                                                                                                  rdx, 3
                                                                                          cmp
; Read the vftable
                                                                           ; Jump to app compat check if the range check fails
488b11
                       rdx, qword ptr [rcx]
               mov
                                                                           7336
                                                                                                  CodeGenTest!DoCast+0x65
                                                                                          jae
                                                                           ; Load the bit vector and do a bit test against it using the ordinal
                                                                           computed
                                                                           488d059af20500 lea
                                                                                                  rax, [CodeGenTest!CastGuardBitVector]
                                                                           480fa310
                                                                                                  qword ptr [rax], rdx
                                                                                          bt
                                                                           ; Jump to a failure stub if the bitmap test fails
                                                                           7330
                                                                                          jae
                                                                                                  CodeGenTest!DoCast+0x6c
                                                                                                  CodeGenTest!DoCast+0x40
                                                                           eb02
                                                                                          jmp
                                                                           ; End of CastGuard check
```

Optimizations

Bitmaps that are all 1's can be turned in to range checks

LHS Type (Type Being Cast To)

| Bitmap bits relative to start of object | | | | | | | | | |
|---|---|---|---|---|---|---|--|--|--|
| A_Bitmap | 1 | 1 | 1 | 1 | 1 | 1 | | | |
| B_Bitmap | 1 | 1 | 0 | 0 | 1 | | | | |
| C_Bitmap | 1 | 1 | 1 | | | | | | |
| D_Bitmap | 1 | 0 | 0 | 1 | | | | | |
| E_Bitmap | 1 | 1 | | | | | | | |
| F_Bitmap | 1 | | | | | | | | |

Optimizations

Bitmaps where all 1's are at fixed offsets from each other don't need a bitmap (but do need to enforce alignment)

| Bitmap bits relative to start of object | | | | | | | | |
|---|---|---|---|---|---|---|--|--|
| A_Bitmap | 1 | 1 | 1 | 1 | 1 | 1 | | |
| B_Bitmap | 1 | 1 | 0 | 0 | 1 | | | |
| C_Bitmap | 1 | 1 | 1 | | | | | |
| D_Bitmap | 1 | 0 | 0 | 1 | | | | |
| E_Bitmap | 1 | 1 | | | | | | |
| F_Bitmap | 1 | | | | | | | |

- Find pointer delta (current_vftable address_of_D_vftable)
- 2. Compute the ordinal by shifting the delta
- 3. Do a range check on the ordinal

Optimizations

Prefer to not use vftables that have virtual base inheritance.

Example:

RHS type and LHS type each have 2 vftable's

1 vftable is part of a virtual-base class hierarchy

1 vftable is part of a normal inheritance class hierarchy

Do the CastGuard check on the vftable from the normal inheritance hierarchy so that no virtual base overhead is needed

Misc

One Definition Rule (ODR) Violations

Sometimes can be detected at compile time (i.e. ODR violation occurs in LTCG code)

To ease adoption, CastGuard will not protect these hierarchies but won't error.

If detected at link time (i.e. static lib introduces ODR violation):

If static lib introduces a smaller or identically sized vftable, we keep the vftable already placed in CastGuard region by the compiler.

If static lib introduces a bigger vftable, linker must select this vftable (documented behavior). This would break CastGuard, so we throw a linker error.