# black hat

DECEMBER 9-10 BRIEFINGS

### efiXplorer Hunting for UEFI Firmware Vulnerabilities at Scale with Automated Static Analysis

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## **black hat** USA 2015

### DISTRIBUTING THE RECONSTRUCTION OF HIGH-LEVEL INTERMEDIATE REPRESENTATION FOR LARGE SCALE MALWARE ANALYSIS

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https://www.blackhat.com/docs/us-15/materials/us-15-Branco-Distributing-The-Reconstruction-Of-High-Level-Intermediate-Representation-For-Large-Scale-Malware-Analysis.pdf #BHEU @BLACKHATEVENTS



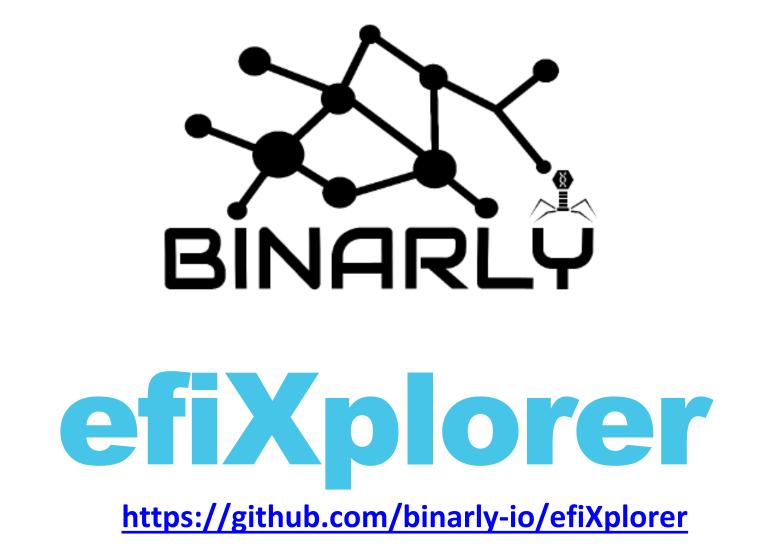


### Outline

- ✓ Why have we created efiXplorer?
- ✓ Motivation
- ✓ Automated vulnerability search
  - Methodology
  - SMM Callout vuln pattern
  - GetVariable/SmmGetVariable vuln pattern
  - PPI GetVariable vuln pattern
- ✓ Final statistics
- ✓ Future Plans











### The UEFI firmware code REconstruction limitations

int16\_t\* sub\_2bc(int64\_t arg1, int64\_t arg2)

000002cb	int64_t rax = *(arg2 + 0x58)
000002cf	int64_t rbx = *(arg2 + 0x60)
000002d6	*data_13498 = arg1
000002dd	*data_13488 = arg2
000002ef	$\star$ data_13490 = rbx
000002f6	*data_134a0 = rax
00000307	if (sub_38d0(data_110e0, &arg_8) != 0)
00000321	*arg_8 = sub_3c7c(*(rbx + 0x40)(4, 8, &arg_8))
00000336	*(*data_13490 + 0xc0)(data_110e0, arg_8)
0000033c	$rbx = *data_{13490}$
00000353	*data_134a8 = *arg_8
00000361	*(rbx + 0x140)(data_10f50, 0, data_134c0)
0000037e	*(*data_13490 + 0x140)(data_bd00, 0, data_134c8)
0000039b	*(*data_13490 + 0x140)(data_bd10, 0, data_134d0)
000003b8	*(*data_13490 + 0x140)(data_bcd0, 0, data_134b8)
000003d5	*(*data_13490 + 0x140)(data_bd80, 0, data_134b0)
000003e9	sub_38d0(data_bd60, data_134d8)
000003fc	sub_38d0(data_10ee0, data_134e0)
00000421	<pre>*data_14350 = sub_b2b0(data_10f20, arg1, data_bde0)</pre>
00000432	*data_13ca8 = *(*data_134d8 + 0xc)
0000043c	int16_t* rax_12 = sub_38d0(data_bd60, &arg_10)





F Decompile: FUN\_000102bc - (AmtWrapperDxe)

void FUN\_000102bc(EFI\_HANDLE ImageHandle26,EFI\_SYSTEM\_TABLE \*SystemTable) byte bVar1; longlong lVar2; ulonglong uVar3; undefined \*puVar4; EFI\_BOOT\_SERVICES \*pEVar5; longlong \*local\_res8; short \*local\_res10; gRT = SystemTable->RuntimeServices; pEVar5 = (EFI\_BOOT\_SERVICES \*)SystemTable->BootServices; **qST** = SystemTable; qBS = pEVar5;gImageHandle = ImageHandle26; lVar2 = FUN\_000138d0((longlong \*)&EfiTscFrequencyGuid,(longlong \*)&local\_res8); if (lVar2 != 0) { (\*pEVar5->AllocatePool)(EfiBootServicesData,8,&local\_res8); lVar2 = FUN\_00013c7c(); \*local\_res8 = lVar2; (\*gBS->InstallConfigurationTable)((EFI\_GUID \*)&EfiTscFrequencyGuid,local\_res8); pEVar5 = gBS;DAT\_000234a8 = \*local\_res8; (**\*pEVar5**->LocateProtocol) ((EFI\_GUID \*)&EfiHiiStringProtocolGuid, (void \*)0x0,&gEFI\_HII\_STRING\_PROTOCOL39); (**\*gBS**->LocateProtocol) ((EFI GUID \*)&EfiHiiDatabaseProtocolGuid, (void \*)0x0,&gEFI HII DATABASE PROTOCOL49); (**\*qBS**->LocateProtocol) ((EFI\_GUID \*)&EfiHiiConfigRoutingProtocolGuid,(void \*)0x0, &gEFI\_HII\_CONFIG\_ROUTING\_PROTOCOL29); (\*gBS->LocateProtocol)((EFI\_GUID \*)&EfiHiiFontProtocolGuid,(void \*)0x0,&gEFI\_HII\_FONT\_PROTOCOL40); (**\*gBS**->LocateProtocol) ((EFI\_GUID \*)&EfiHiiImageProtocolGuid,(void \*)0x0,&gEFI\_HII\_IMAGE\_PROTOCOL47); FUN\_000138d0((longlong \*)&EfiHobListGuid,&DAT\_000234d8); FUN\_000138d0((longlong \*)&EfiDxeServicesTableGuid,&DAT\_000234e0); DAT 00024350 = FUN\_0001b2b0((undefined8 \*)&EfiPhysicalPresenceGuid,ImageHandle26,(int \*)&DAT\_0001bde0,0); \_DAT\_00023ca8 = \*(int \*)(DAT\_000234d8 + 0xc); lVar2 = FUN\_000138d0((longlong \*)&EfiHobListGuid,(longlong \*)&local\_res10);





### Why we work on efiXplorer?

- Simplifying Reconstruction of UEFI-specific types and protocols efiXplorer->efiAnalyzer
- Creating a unified loader for whole UEFI firmware image with rebuilt dependencies and cross-references between different DXE and PEI modules
  - efiXplorer->efiLoader
- Finding common types of vulnerabilities with UEFI specifics and power of static analysis
  - efiXplorer->efiAnalyzer->efiVuInHunt



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### Hex-Rays + efiXplorer

```
fastcall * fastcall sub 2BC(void *a1, EFI SYSTEM TABLE *a2))()
int64
EFI_RUNTIME_SERVICES *v2; // rax
EFI BOOT SERVICES *v3; // rbx
 int64 i; // rax
char v6; // cl
char v7; // bl
 _int64 v8; // rax
 _int64 v9; // rdx
 int64 ( fastcall *result)(); // rax
UINTN DataSize; // [rsp+50h] [rbp+20h] BYREF
int64 v12; // [rsp+58h] [rbp+28h] BYREF
v2 = a2->RuntimeServices;
v3 = a2->BootServices;
AgentHandle = a1;
gST 13488 = a2;
gBS 13490 = v3;
gRT 134A0 = v2;
if ( sub 38D0(&EFI TSC FREQUENCY GUID 110E0, &DataSize) )
  (v3->AllocatePool)(4i64, 8i64, &DataSize);
  *DataSize = sub 3C7C();
  gBS_13490->InstallConfigurationTable(&EFI_TSC_FREQUENCY GUID 110E0, DataSize);
  v3 = gBS_{13490};
gword 134A8 = *DataSize;
(v3->LocateProtocol)(&EFI_HII_STRING_PROTOCOL_GUID_10F50, 0i64, &qword_134C0);
gBS_13490->LocateProtocol(&EFI_HII_DATABASE_PROTOCOL_GUID_BD00, 0164, &Interface);
gBS_13490->LocateProtocol(&EFI_HII_CONFIG_ROUTING_PROTOCOL_GUID_BD10, 0i64, &qword_134D0);
gBS_13490->LocateProtocol(&EFI_HII_FONT_PROTOCOL_GUID_BCD0, 0i64, &qword_134B8);
gBS 13490->LocateProtocol(&EFI HII IMAGE PROTOCOL GUID BD80, 0i64, &qword 134B0);
SUD_38D0(&EFI_HOB_LISI_GUID_BD60, &qword_134D8);
sub 38D0(&DXE SERVICES TABLE GUID 10EE0, &gword 134E0);
qword_14350 = sub_B2B0(&EFI_PHYSICAL_PRESENCE_DATA_GUID 10F20, a1, &unk_BDE0, 0i64);
dword 13CA8 = *(qword 134D8 + 12);
```





### efiXloader: SMI handlers identification

```
9// ----- Function Prototypes ------
.1void sub_21B4(int64_t a1, int64_t a2);
2void SwSmiHandler 11E4(void);
6int64 t qword 4168 = 0; // 0x4168
.7int64_t qword_42A0 = 0; // 0x42a0
1// Address range: 0x11e4 - 0x12ad
2void SwSmiHandler_11E4(void) {
    uint64_t v1 = *(int64_t *)(qword_4168 + 104); // 0x122e
    if (v1 == 0) {
       // 0x1298
       *(int32_t *)&qword_42A0 = *(int32_t *)24;
       return;
    int64 t v_2 = 0; // 0x1247
    int64_t v3 = *(int64_t *)(qword_4168 + 112); // 0x11e4
    sub_21B4(v2, v3);
    v2++;
    v3 += 24;
    while (v2 < v1) {
       // 0x126a
       sub_21B4(v2, v3);
       v2++;
       v3 += 24;
    // 0x1298
    *(int32 t *)&qword 42A0 = *(int32 t *)24;
    ----- Meta-Information ------
```

```
__int64 SwSmiHandler_11E4()
 __int64 v1; // [rsp+20h] [rbp-28h]
 unsigned __int64 i; // [rsp+28h] [rbp-20h]
 __int64 v3; // [rsp+30h] [rbp-18h]
 unsigned __int64 v4; // [rsp+38h] [rbp-10h]
 v1 = 0i64;
 v3 = *(QWORD *)(qword 4168 + 112);
 v4 = *(_QWORD *)(qword_4168 + 104);
 for ( i = 0i64; i < v4; ++i )
   if ( !sub 21B4(v3, &EFI SMBIOS TABLE GUID 3000, 16i64) )
     v1 = *(_QWORD *)(v3 + 16);
     break;
   v3 += 24i64;
 LODWORD(qword_{42A0}) = *(_DWORD *)(v1 + 24);
 return 0i64;
```





EVENTS



### How it started,

efiAnalyzer	
dataGuids	getBsProtNamesX64
allProtocols	getBsProtNamesX86
allServices	getSmmProtNamesX64
findImageHandleX64	getAllPeiServices
findSystemTableX64	printProtocols
findBootServicesTables	markProtocols
findRuntimeServicesTables	markDataGuids
findSmstX64	markLocalGuidsX64
findOtherBsTablesX64	findSwSmiHandler
getProtBootServicesX64	findSmmCallout
getProtBootServicesX86	dumpInfo
getAllBootServices	efiAnalyzer
getAllRuntimeServices	~efiAnalyzer
getAllSmmServicesX64	fileType

(86)

### and how it's going?

iAnalyzer	
PUBLIC	
ataGuids	getSm
llProtocols	getAl
llPPIs	getPp
llServices	getAl
etSegments	print
indImageHandleX64	mark]
indSystemTableX64	mark[
indBootServicesTables	markl
indRuntimeServicesTables	finds
indSmstX64	find
indOtherBsTablesX64	findF
etProtBootServicesX64	finds
etProtBootServicesX86	finds
etAllBootServices	dump]
etAllRuntimeServices	efiAr
etAllSmmServicesX64	~efi#
etBsProtNamesX64	file
atBcBrotNamosV86	



ProtNamesX64	
PeiServicesX86	
NamesX86	
VariablePPICallsX86	
nterfaces	
terfaces	
taGuids	
calGuidsX64	
SmiHandlers	
tVariableOveflow	
IGetVariableStackOveflow	
mGetVariableOveflow	
mCallout	
fo	
lyzer	
alyzer	
pe	
	KHATEVENTS





	First prize Dy	nDataResolver	
Second prize	Lucid and grap	Third prize	efiXplo

https://www.hex-rays.com/contests\_details/contest2020/





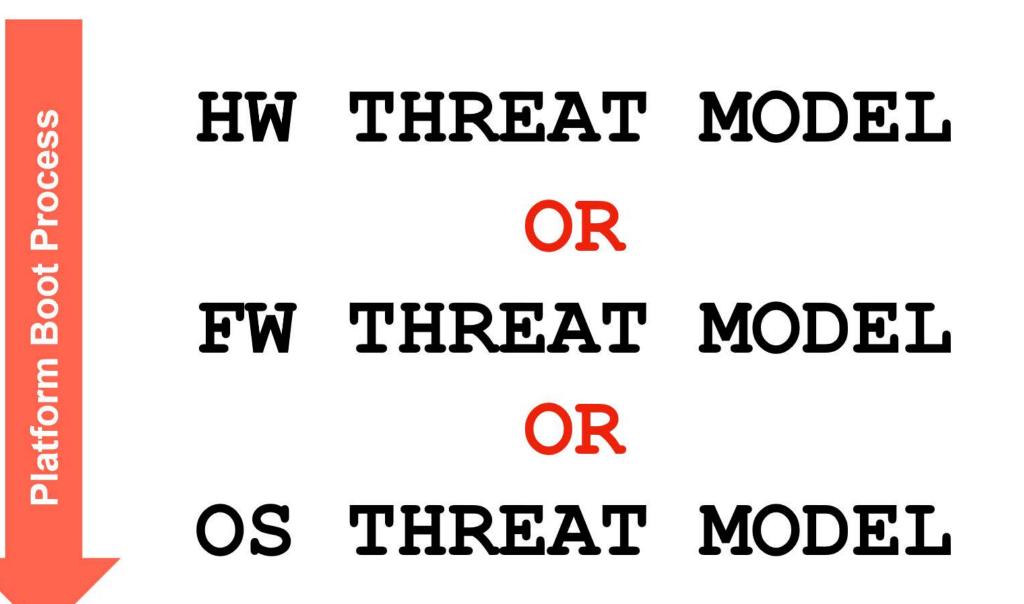


# **Motivation of this** REsearch







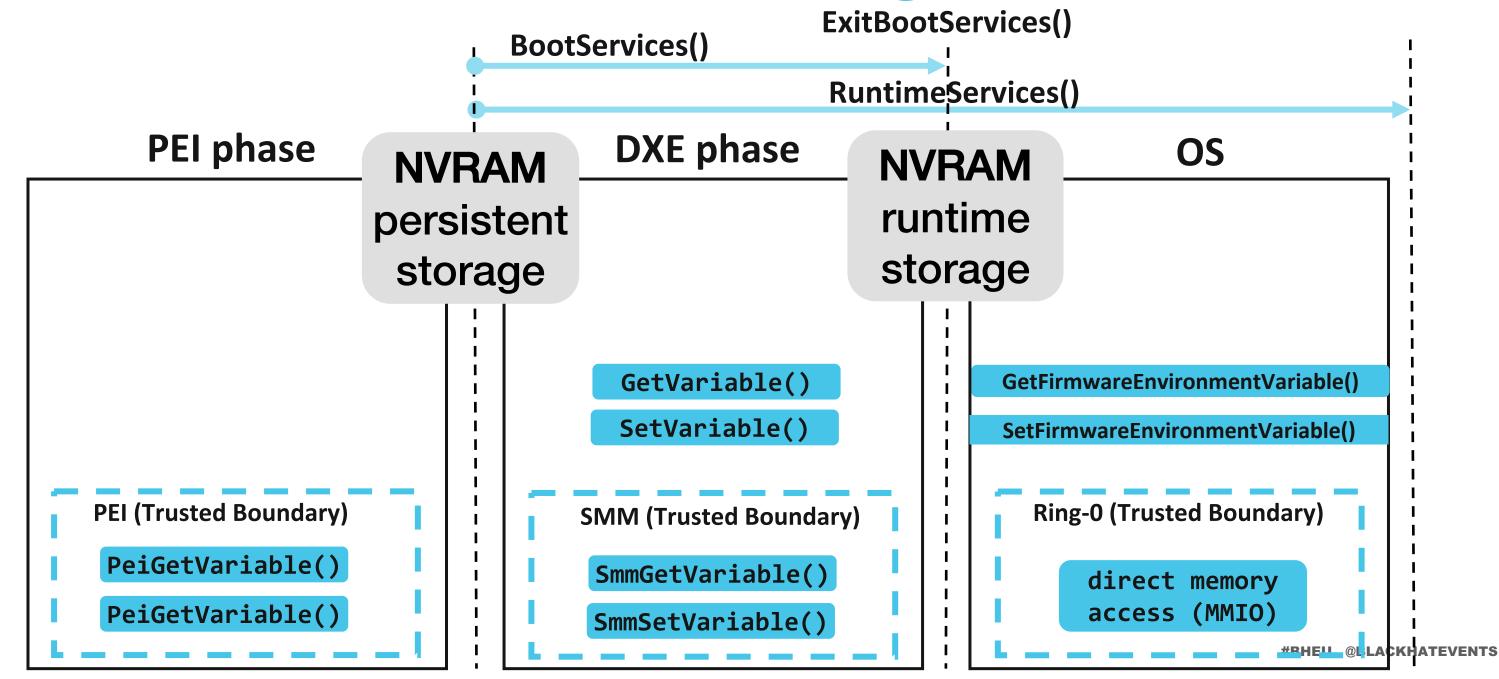


# Lack of Threat Intel Signals

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### **NVRAM Variables access during Boot Flow**







### **NVRAM persistence on SPI flash**

BIOS region	Region	BIOS	
FA4974FC-AF1D-4E5D-BDC5-DACD6D27BAEC	Volume	FFSv2	
✓ NVRAM	File	Raw	NVAR store
✓ 4599D26F-1A11-49B8-B91F-858745CFF824	NVAR entry	Full	StdDefaults
EfiSetupVariableGuid	NVAR entry	Full	Setup
EfiGlobalVariableGuid	NVAR entry	Full	PlatformLang
EfiGlobalVariableGuid	NVAR entry	Full	Timeout
C811FA38-42C8-4579-A9BB-60E94EDDFB	NVAR entry	Full	AMITSESetup
90D93E09-4E91-4B3D-8C77-C82FF10E3C	NVAR entry	Full	CpuSmm
5432122D-D034-49D2-A6DE-65A829EB4C	NVAR entry	Full	MeSetupStorage
64192DCA-D034-49D2-A6DE-65A829EB4C	NVAR entry	Full	IccAdvancedSetupDataV
69ECC1BE-A981-446D-8EB6-AF0E53D06C	NVAR entry	Full	NewOptionPolicy
D1405D16-7AFC-4695-BB12-41459D3695	NVAR entry	Full	NetworkStackVar
EfiSetupVariableGuid	NVAR entry	Full	SdioDevConfiguration
EfiSetupVariableGuid	NVAR entry	Full	UsbSupport

- Visit NVRAM region is not protected by Intel Boot Guard and can be abused by attacker with physical access (supply chain vector).
- ✓ Arbitrary code execution via GetVariable() is common, attacker can modify persistent NVRAM storage and install fileless DXE/SMM/PEI implant (shellcode).

Most security solutions inspect only UEFI drivers!







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### **NVRAM persistence on SPI flash**

✓BIOS region	Region	BIOS	
✓ FA4974FC-AF1D-4E5D-BDC5-DACD6D27BAEC	Volume	FFSv2	
✓ NVRAM	File	Raw	NVAR store
✓4599D26F-1A11-49B8-B91F-858745CFF824	NVAR entry	Full	StdDefaults
EfiSetupVariableGuid	NVAR entry	Full	Setup
EfiGlobalVariableGuid	NVAR entry	Full	PlatformLang

```
int64 __fastcall get_variable(CHAR16 *VariableName, EFI_GUID *VendorGuid, void **a3, UINTN *a4)
__int64 result; // rax
void *Data; // rax
__int64 v10; // rsi
UINTN DataSize; // [rsp+50h] [rbp+18h] BYREF
DataSize = 0i64; size == NULL
*a3 = 0164;
if ( a4 )
                                                              Controlled Size
                                PlatformLang
  *a4 = 0i64;
result = gRT_13BE0->GetVariable(VariableName, VendorGuid, 0i64, &DataSize, *a3);
```





### +Buffer] BAL\_VARIABLE\_GUID\_11858 rmlang ; "PlatformLang"





### **NVRAM persistence: previous work**

### ✓ Linux NVRAM runtime persistence (not SPI storage)

https://media.defcon.org/DEF%20CON%2027/DEF%20CON%2027%20presentations/DEFCON-27-Michael-Leibowitzand-Topher-Timzen-EDR-Is-Coming-Hide-Yo-Sht.pdf

https://github.com/perturbed-platypus/LinooxMalware

### ✓ MS Win NVRAM runtime persistence (not SPI storage)

https://slaeryan.github.io/posts/midnighttrain.html

https://github.com/slaeryan/MIDNIGHTTRAIN

\* NVRAM persistent storage (with physical access to the target machine) also mentioned in CIA Vault7 leak



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### efi\_fuzz: Groundwork to the Metaphysics of coverage-guided UEFI fuzzing

Assaf Carlsbad

Itai Liba

Location: Station 2

Date: Thursday, December 10 | 1:00pm-2:00pm

Track: (
) Hardware/Embedded

Session Type: Arsenal

carlsbad@DESKTOP-VN7FI5S:/mnt/c/Users/Assaf/Work/efi\_fuzz-private\$ python3 efi\_fuzz.py ../efi\_fuzz/samples/SystemSmmAhciAspiLegacyRt\_body.efi ../efi\_fuzz/nvram.pickle Setup requir ements.txt --output=trace -n [+] Initiate stack address at 0x7ffffffde000 [+] Loading ../efi\_fuzz/samples/SystemSmmAhciAspiLegacyRt\_body.efi to 0x10000 [+] PE entry point at 0x104dc [+] Done with loading ../efi\_fuzz/samples/SystemSmmAhciAspiLegacyRt\_body.efi [+] Running from 0x104dc of ../efi\_fuzz/samples/SystemSmmAhciAspiLegacyRt\_body.efi LocateProtocol(Protocol = "1390954d-da95-4227-9328-7282c217daa8", Registration = 0x0, Interface = 0x10c08) = 0x0 LocateProtocol(Protocol = "d2b2b828-0826-48a7-b3df-983c006024f0", Registration = 0x0, Interface = 0x10c10) = 0x8000000000000000000 LocateProtocol(Protocol = "6afd2b77-98c1-4acd-a6f9-8a9439de0fb1", Registration = 0x0, Interface = 0x10c18) = 0x800000000000000000 HandleProtocol(Handle = 0x10000, Protocol = "5b1b31a1-9562-11d2-8e3f-00a0c969723b", Interface = 0x10c00) = 0x0 InSmm(This = 0x500100080, InSmram = 0x80000001d010) GetSmstLocation(This = 0x500100080, Smst = 0x10c20) = 0x0 LocateProtocol(Protocol = "e541b773-dd11-420c-b026-df993653f8bf", Registration = 0x0, Interface = 0x80000001cfd8) = 0x0 GetSmstLocation(This = 0x500100080, Smst = 0x10c40) = 0x0 LocateProtocol(Protocol = "ff052503-1af9-4aeb-83c4-c2d4ceb10ca3", Registration = 0x0, Interface = 0x80000001d018) = 0x0 AllocatePool(PoolType = 0x6, Size = 0x800, Buffer = 0x10c38) = 0x0 LocateProtocol(Protocol = "eb346b97-975f-4a9f-8b22-f8e92bb3d569", Registration = 0x0, Interface = 0x10bb8) = 0x0 Func1(Arg1 = 0x10240, Arg2 = 0x10250) = 0x0SMM\_SW\_DISPATCH\_Register(This = 0x500100070, DispatchFunction = 0x103dc, RegisterContext = 0x80000001cfd0, DispatchHandle = 0x80000001cfe0) \*\*\* done with ../efi\_fuzz/samples/SystemSmmAhciAspiLegacyRt\_body.efi, 0 Executing SMI with params {'This': 21475885168, 'DispatchFunction': 66524, 'RegisterContext': 140737488474064, 'DispatchHandle': 140737488474080} \*\*\* read\_from\_system - 16, 0x5000002b0, 8, 0 SMI handler tried to call a boot service

https://labs.sentinelone.com/moving-from-dynamic-emulation-of-uefi-modules-to-coverage-guided-fuzzing-of-uefi-firmware/



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X



### Limitations of blackbox AFL fuzzing

- $\checkmark$  Lack of code-coverage-based feedback loop means test generation can rely only static corpus.
- Random input mutations with little initial knowledge may need extra RE work to create more precise/valid corpus
- V Platform simulation like Simics with combination of Symbolic Execution\* can improve input corpus generation and test coverage in general.
- $\checkmark$  efiXplorer can also fill that gap by providing the coverage and helping with corpus generation for potential targets.

\* https://software.intel.com/content/www/us/en/develop/articles/finding-bios-vulnerabilities-with-symbolic-executionand-virtual-platforms.html







### **Vendors disclosure Details**

### Intel/Dell Timeline (discovered by Nvidia Offensive Research):

- Sep 2020: Initial Disclosure
- Oct 2020: Issues confirmed

**GetVariable()** – 2 stack overflow issues with **SMM code execution impact SmmGetVariable()** – 2 stack overflow issues with **SMM code execution impact CommBuffer** – 1 heap overflow issue with **SMM code execution impact** 

- Nov 2020: Security fixes confirmed in update cycle
- April 2020: Disclosure date





# efixplorer Automated vulnerability search at scale







### Automated vulnerability search methodology

### We used 3 datasets with firmware images only released in 2020:

- ✓ **ASRock** 450 firmware images
- ✓ **ASUS** 820 firmware images
- ✓ **Lenovo** 84 firmware images



### Automated vulnerability search methodology

We evaluated efixplorer at automated vulnerability search in three ways:

- Measuring objects and structures recovery  $\checkmark$ ✓ Function calls recovery precision 0.94 / recall 0.88 (at DXE stage) ✓ For more info: https://github.com/binarly-io/Research Publications/tree/main/EKO 2020
- Measuring attack surface: number of SMI handlers and GetVariable calls
- Running automated vulnerability checks and validating results semi-manually

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# efiXplorer SMI callout automated search





- SMM callout is a well-known attack vector for years and retains its significant place in the UEFI firmware security assessment
- SMI handlers are crucial places, where SMM callouts may exist
- Assume that some Runtime Service triggers inside SMI handler



### **retains its ent** exist dler



- efiXloader introduces the semi-automatic way of SMM callouts identification within the whole firmware using static analysis approach
- Since efiXloader can trigger efiXplorer analyzing routines, it is possible to identify SMM callouts within the whole firmware
- Runtime/Boot services execution inside SMM

Iterate through EFI\_SMM\_SW\_DISPATCH2\_PROTOCOL.Register() within each SMM driver and collect pointer to SMI handler





Iterate through EFI\_SMM\_SW\_DISPATCH2\_PROTOCOL.Register() within each SMM driver and collect pointer to SMI handler

000000000090F49C	mov	rax, rsp
000000000090F49F	sub	rsp, 38h
000000000090F4A3	and	qword ptr [ <mark>rax</mark> +18h], 0
000000000090F4A8	lea	r8, [ <mark>rax</mark> +18h] ; Interface
000000000090F4AC	and	qword ptr [ <mark>rax</mark> +20h], 0
000000000090F4B1	lea	<pre>rcx, large EFI_SMM_SW_DISPATCH2_PROTOCOL_GUID_90F580 ; Protocol</pre>
000000000090F4B8	mov	qword ptr [ <mark>rax</mark> -18h], 0BEh
000000000090F4C0	xor	edx, edx ; Registration
000000000090F4C2	mov	<pre>rax, large cs:gSmst_90F5C8</pre>
000000000090F4C9	call	[rax+_EFI_SMM_SYSTEM_TABLE2.SmmLocateProtocol] ; gSmst->SmmLocateProto
000000000090F4CF	test	rax, rax
000000000090F4D2	js	short loc_90F4EF
000000000090F4D4	mov	<mark>rax</mark> , [rsp+50h]
000000000090F4D9	lea	r9, [rsp+88]
000000000090F4DE	lea	r8, [rsp+32]
000000000090F4E3	mov	rcx, <mark>rax</mark>
000000000090F4E6	lea	rdx, SwSmiHandler_90F480
00000000090F4ED	call	qword ptr [ <mark>rax</mark> ] ; SMI handler registration
000000000090F4EF		
000000000090F4EF loc_90F4EF:		; CODE XREF: sub_90F49C+36↑j
000000000090F4EF	add	rsp, 38h
000000000090F4F3	retn	
000000000090F4F3 sub_90F49C	endp	
000000000090F4F3		
000000000090F4F4 ; [00000003	BYTES: COL	LAPSED FUNCTION nullsub_26. PRESS CTRL-NUMPAD+ TO EXPAND]
000000000090F4F7	db ØCCI	h
000000000090F4F8		



tocol



Iterate through EFI SMM SW DISPATCH2 PROTOCOL.Register() within each SMM driver and collect pointer to SMI handler

```
int64 result; // rax
int64 v1[3]; // [rsp+20h] [rbp-18h] BYREF
EFI_SMM_SW_DISPATCH2_PROTOCOL *v2; // [rsp+50h] [rbp+18h] BYREF
__int64 v3; // [rsp+58h] [rbp+20h] BYREF
v^2 = 0i64;
v^3 = 0i64;
v1[0] = 190i64;
result = gSmst 90F5C8->SmmLocateProtocol(&EFI SMM SW DISPATCH2 PROTOCOL GUID 90F580, 0i64, &v2);
if (result \geq 0)
  result = (v2->Register)(v2, SwSmiHandler 90F480, v1, &v3);
return result;
```





### BootServices

```
/* find callouts with gBS */
for (vector<ea_t>::iterator bs = gBsList.begin(); bs != gBsList.end();
    ++bs) {
    /* check if insn is 'mov rax, cs:gBS' */
       (insn.itype == NN_mov && insn.ops[0].reg == REG_RAX &&
        insn.ops[1].type == o_mem && insn.ops[1].addr == *bs) {
        DEBUG_MSG("[%s] SMM callout found: 0x%016X\n", plugin_name,
                  ea);
        calloutAddrs.push_back(ea);
```



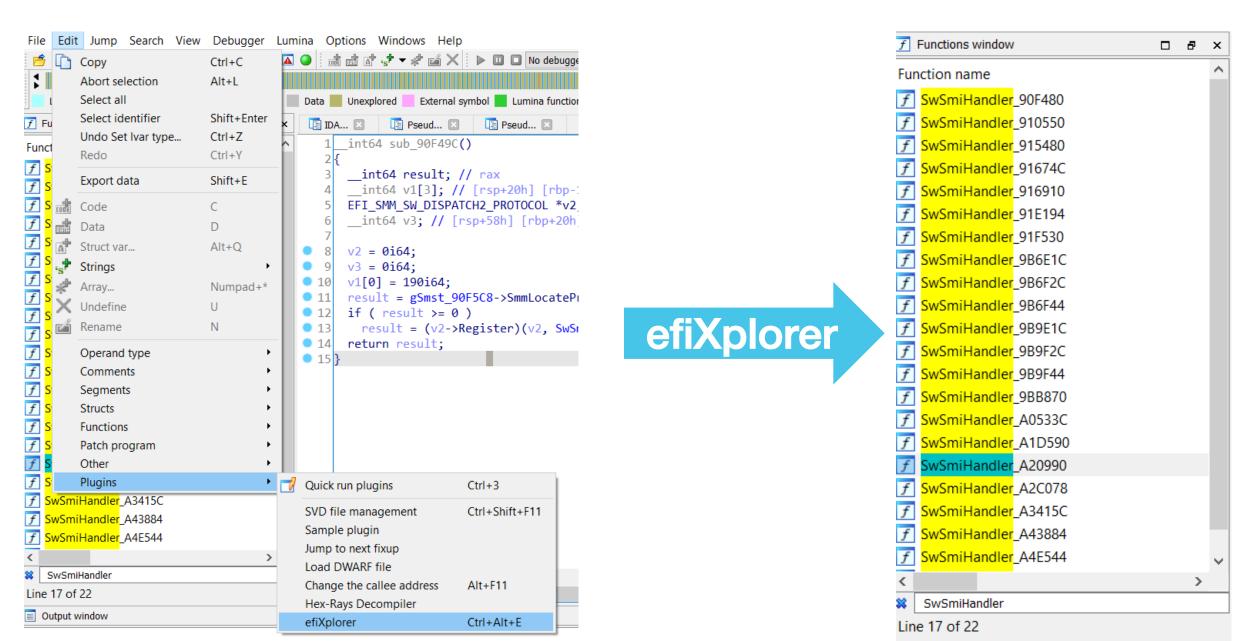


### RuntimeServices

```
/* find callouts with gRT */
   (vector<ea_t>::iterator rt = gRtList.begin(); rt != gRtList.end();
for
    ++rt) {
    /* check if insn is 'mov rax, cs:gRT' */
   if (insn.itype == NN_mov && insn.ops[0].reg == REG_RAX &&
        insn.ops[1].type == o_mem && insn.ops[1].addr == *rt) {
        DEBUG_MSG("[%s] SMM callout found: 0x%016X\n", plugin_name,
                  ea);
        calloutAddrs.push_back(ea);
```













### **SMM callouts identification: statistics**

Vendor Name	Avg number of SMI calls per firmware	Avg number of SMM calle triggered per firm
ASRock	51	72
ASUS	42	80
Lenovo	20	3



### lout pattern is mware



### SMM callouts identification: results

1_	_int64fastcall SwSmiHandler_48C()
2{	
3	int v0; // edi
4	_EFI_SMM_SYSTEM_TABLE2 *v1; // rax

```
v1 = gSmst_1AF8;
 v2 = 0i64;
 if ( gSmst_1AF8->NumberOfCpus )
   while ( gSmmCpu_1C10->ReadSaveState(gSmmCpu_1C10, 0x18ui64, EFI_SMM_SAVE_STATE_REGISTER_IO, v2, Buffer)
        || v51 != 178 )
     v3 = ++v2 == gSmst_1AF8->NumberOfCpus;
     if ( v2 >= gSmst_1AF8->NumberOfCpus )
       goto LABEL_8;
   v1 = gSmst_1AF8;
 v3 = v2 == v1->NumberOfCpus;
LABEL 8:
if ( !v3 )
   gSmmCpu 1C10->ReadSaveState(gSmmCpu 1C10, 4ui64, EFI SMM SAVE STATE REGISTER RAX, v2, &v44);
  gSmmCpu_1C10->ReadSaveState(gSmmCpu_1C10, 4ui64, EFI_SMM_SAVE_STATE_REGISTER_RBX, v2, &v45)
   gSmmCpu_1C10->ReadSaveState(gSmmCpu_1C10, 4ui64, EFI_SMM_SAVE_STATE_REGISTER_RCX, v2, &v46);
   gSmmCpu_1C10->ReadSaveState(gSmmCpu_1C10, 4ui64, EFI_SMM_SAVE_STATE_REGISTER_RDX, v2, &v47);
   gSmmCpu_1C10->ReadSaveState(gSmmCpu_1C10, 4ui64, EFI_SMM_SAVE_STATE_REGISTER_RSI, v2, &v48);
   gSmmCpu_1C10->ReadSaveState(gSmmCpu_1C10, 4ui64, EFI_SMM_SAVE_STATE_REGISTER_RDI, v2, &v49);
```

```
v45 != 8475 )
switch ( v45 )
  case 0x7003u:
   goto LABEL 132;
  case 0x8271u:
    sub 13F4(&v44);
    goto LABEL_132;
  case 0x8290u:
   v45 &= 0xFFFF0000;
   v46 &= 0xFFFF0000;
    goto LABEL_130;
  case 0x8291u:
   v45 = v45 \& 0xFFFF0001 | 1;
   v46 = v46 & 0xFFFF0020
    goto LABEL 130;
goto LABEL_112;
```



### gRT\_1B00->ResetSystem(EfiResetShutdown, 0i64, 0i64, 0i64);

0x20;



# efiXplorer GetVariable vuln search





### efiXplorer: GetVariable vuln search

### EFI\_GET\_VARIABLE EFI\_RUNTIME\_SERVICES::GetVariable definition

	621	/**	
	622	Returns the value of a variable.	
	623		
	624	@param[in] VariableName A N	ull-terminated string that is the name of the vendor's
	625	var	iable.
	626	@param[in] VendorGuid A u	nique identifier for the vendor.
	627	@param[out] Attributes If	not NULL, a pointer to the memory location to return the
	628	att	ributes bitmask for the variable.
	629	@param[in, out] DataSize On :	input, the size in bytes of the return Data buffer.
	630	On (	output the size of data returned in Data.
	631	@param[out] Data The	buffer to return the contents of the variable. May be NULL
	632	wit	h a zero DataSize in order to determine the size buffer needed.
	633		
	634		function completed successfully.
	635	C	variable was not found.
	636		DataSize is too small for the result.
	637	@retval EFI_INVALID_PARAMETER Var:	
	638	@retval EFI_INVALID_PARAMETER Ven	
	639	@retval EFI_INVALID_PARAMETER Data	
	640		DataSize is not too small and Data is NULL.
	641	C	variable could not be retrieved due to a hardware error.
	642	@retval EFI_SECURITY_VIOLATION The	variable could not be retrieved due to an authentication failure.
	643		
	644	**/	
	645	typedef	
	646	EFI_STATUS	
••	• 647	(EFIAPI *EFI_GET_VARIABLE)(	91/
	648	IN CHAR16	*VariableName,
	649	IN EFI_GUID	*VendorGuid,
	650	OUT UINT32	*Attributes, OPTIONAL
	651	IN OUT UINTN	*DataSize,
	652	OUT VOID	*Data OPTIONAL
	653	);	

https://github.com/tianocore/edk2/blob/3806e1fd139775610d8f2e7541a916c3a91ad989/MdePkg/Include/Uefi/UefiSpec.h#L647





### efiXplorer: GetVariable vuln search

If DataSize smaller than VarDataSize, just change DataSize and return EFI\_BUFFER\_TOO\_SMALL status code (according to the implementation of VariableServiceGetVariable from EDK2)

```
2377
            11
   2378
            // Get data size
            11
   2379
   2380
            VarDataSize = DataSizeOfVariable (Variable.CurrPtr, mVariableModuleGlobal->VariableGlobal.AuthFormat);
   2381
            ASSERT (VarDataSize != 0);
   2382
   2383
            if (*DataSize >= VarDataSize) {
   2384
              if (Data == NULL) {
                Status = EFI_INVALID_PARAMETER;
   2386
                goto Done:
   2387
   2388
   2389
              CopyMem (Data, GetVariableDataPtr (Variable.CurrPtr, mVariableModuleGlobal->VariableGlobal.AuthFormat), VarDataSize);
   2390
   2391
              *DataSize = VarDataSize;
   2392
              UpdateVariableInfo (VariableName, VendorGuid, Variable.Volatile, TRUE, FALSE, FALSE, &gVariableInfo);
   2393
   2394
              Status = EFI_SUCCESS;
   2395
              goto Done;
   2396
            } else {
              *DataSize = VarDataSize;
··· 2397
   2398
              Status = EFI BUFFER TOO SMALL;
   2399
              goto Done;
   2400
```

### https://github.com/tianocore/edk2/blob/master/MdeModulePkg/Universal/Variable/RuntimeDxe/Variable.c#L2397





## efiXplorer: GetVariable vuln search

## **Algorithm and implementation**

- loop through all the pairs of **GetVariable** calls and get the address of the **DataSize** stack variable on the first call
- check that the data size is not initialized before the second call to **GetVariable**
- check that the **DataSize** argument variable is the same for two calls

```
check DataSize initialization */
bool init ok = false;
decode insn(&insn, prev head(curr addr, 0));
if (!wrong_detection &&
    !(insn.itype == NN_mov && insn.ops[0].type == o_displ &&
      (insn.ops[0].phrase == REG_RSP ||
      insn.ops[0].phrase == REG RBP))) {
    init ok = true;
  check that the DataSize argument variable is the same for two
 * calls */
if (init ok) {
    ea = prev_head(static_cast<ea_t>(prev_addr), 0);
    for (auto i = 0; i < 10; ++i) {
        decode_insn(&insn, ea);
        if (insn.itype == NN_lea && insn.ops[0].type == o_reg &&
            insn.ops[0].reg == REG R9) {
            if (dataSizeStackAddr == insn.ops[1].addr) {
                getVariableOverflow.push_back(curr_addr);
               DEBUG MSG(
                    "[%s] \toverflow can occur here: 0x%016x\n",
                    plugin name, curr addr);
                break:
       ea = prev head(ea, 0);
```





# efiXplorer: GetVariable vuln search

	=======================================		======
	Looking for GetVariable stack/heap		
	GetVariable_1: 0x000000000000374,		
[efiXplorer]	GetVariable_1: 0x0000000000004ff,	GetVariable_2:	0x000000000000050c
[efiXplorer]	GetVariable_1: 0x0000000000000050c,	GetVariable_2:	0x000000000000565
[efiXplorer]	GetVariable_1: 0x0000000000000565,	GetVariable_2:	0x0000000000006f3
[efiXplorer]	GetVariable_1: 0x0000000000006f3,	GetVariable_2:	0x000000000000736
[efiXplorer]	overflow can occur here: 0x000	0000000000736	
[efiXplorer]	GetVariable_1: 0x000000000000736,	GetVariable_2:	0x000000000000960
[efiXplorer]	GetVariable_1: 0x0000000000000960,	GetVariable_2:	0x000000000000c4f
[efiXplorer]	GetVariable_1: 0x0000000000000c4f,	GetVariable_2:	0x0000000000000c5c
[efiXplorer]	GetVariable_1: 0x0000000000000c5c,	GetVariable_2:	0x000000000000c69
[efiXplorer]	GetVariable_1: 0x000000000000069,	GetVariable_2:	0x0000000000000d58
[efiXplorer]	GetVariable_1: 0x0000000000000d58,	GetVariable_2:	0x0000000000000ef9
[efiXplorer]	GetVariable_1: 0x0000000000000ef9,	GetVariable_2:	0x000000000001337
[efiXplorer]	GetVariable_1: 0x000000000001337,	GetVariable_2:	0x000000000001344
[efiXplorer]	GetVariable_1: 0x000000000001344,	GetVariable_2:	0x000000000001351
[efiXplorer]	GetVariable_1: 0x000000000001351,	GetVariable_2:	0x000000000001396
[efiXplorer]	GetVariable_1: 0x000000000001396,	GetVariable_2:	0x00000000000149b
[efiXplorer]	GetVariable_1: 0x00000000000149b,	GetVariable_2:	0x000000000001530
[efiXplorer]	GetVariable_1: 0x0000000000001530,	GetVariable_2:	0x0000000000015d3
[efiXplorer]	GetVariable_1: 0x00000000000015d3,	GetVariable_2:	0x0000000000016d8
[efiXplorer]	GetVariable_1: 0x00000000000016d8,	GetVariable_2:	0x0000000000001729
[efiXplorer]	GetVariable_1: 0x000000000001729,	GetVariable_2:	0x00000000000181d
	=======================================		======
[efiXplorer]	Looking for SmmGetVariable stack/h	eap overflow	
	gSmmVar->SmmGetVariable calls find:		— — —
[efiXplorer]	gSmmVar->SmmGetVariable function f:	inding from 0x0	000000000001A60 to 0x0000000000000
[efiXplorer]	<pre>can't find a EFI_SMM_VARIABLE_PROT(</pre>	OCOL_GUID guid	
[efiXplorer]	less than 2 GetVariable calls found	d	
[efiXplorer]	=======================================		======





01EE0



# efiXplorer: GetVariable vuln examples

In this case, changing the value of the variable can lead to the execution of arbitrary code 

```
WORD *StringPtr; // r11
int64 status; // rax
char Data[424]; // [rsp+40h] [rbp-1A8h] BYREF
int64 StringSize; // [rsp+1F8h] [rbp+10h] BYREF
UINTN DataSize; // [rsp+200h] [rbp+18h] BYREF
EFI HII STRING PROTOCOL *HiiStringProtocol; // [rsp+208h] [rbp+20h] BYREF
StringSize = 1280i64;
DataSize = 0i64;
gBS 180007E38->AllocatePool(EfiBootServicesData, 0xA00ui64, String);
StringPtr = *String;
if ( *String < *String + 2 * StringSize )</pre>
   *StringPtr++ = 0;
 while ( StringPtr < (*String + 2 * StringSize) );</pre>
gBS_180007E38->LocateProtocol(&EFI_HII_STRING_PROTOCOL_GUID_180007050, 0i64, &HiiStringProtocol);
status = gRT_180007E40->GetVariable(VariableName, &VendorGuid, 0i64, &DataSize, Data);
if ( status == EFI BUFFER TOO SMALL )
 status = gRT 180007E40->GetVariable(VariableName, &VendorGuid, 0i64, &DataSize, Data);
if ( status < 0 )
 return EFI NOT FOUND;
gBS_180007E38->FreePool(*String);
 return EFI NOT FOUND;
return EFI_SUCCESS;
```







# efiXplorer: GetVariable vuln examples

- The sequence of multiple GetVariable calls may cause the buffer overflow as follows
  - 1. First call is required to update DataSize value
  - 2. Second call trigger OOB write

gBS\_180007970->LocateProtocol(&ProprietaryProtocol\_180007560, 0i64, &Handle); DataSize = 8i64; if ( (gRT 180007950->GetVariable(VariableName1, &VendorGuid, 0i64, &DataSize, &Data1) & 0x800000000000000000 == 0i64 ) ProtocolInterface->Data1 = Data1; if ( (gRT 180007950->GetVariable(VariableName2, &VendorGuid, 0i64, &DataSize, &Data2) & 0x8000000000000000000 == 0i64 ) ProtocolInterface->Data2 = Data2;

```
DataSize = 4i64 * struct->size;
received = 0;
status = gRT 1005B860->GetVariable(VariableName1, &VendorGuid1, 0i64, &DataSize, struct->Data1);
status = gRT 1005B860->GetVariable(VariableName2, &VendorGuid2, 0i64, &DataSize, struct->Data2);
received = 1;
```

## Correct usage

Initializing data size before each call

<pre>protocolInterface = ProtocolInterface;</pre>	
DataSize = 8i64;	
gRT_180007950->GetVariable(&VariableName_1, &VenorGuid, 0i64, &DataSi	<mark>ze,</mark> &Protoc
DataSize = 8i64;	
gRT_180007950->GetVariable(&VariableName_2, &VenorGuid, 0i64, &DataSi	ze, &protoc
DataSize = 8i64;	
gRT_180007950->GetVariable(&VariableName_3, &VenorGuid, 0i64, &DataSi	ze, &protoc
DataSize = 8i64;	



- colInterface->Data 1);
- colInterface->Data 2);
- colInterface->Data 3);



## **GetVariable vuln search: statistics**

Vendor Name	Avg number of calls per firmware	Avg number of vuln pattern firmware
ASRock	735	2
ASUS	697	5
Lenovo	<b>466</b>	20



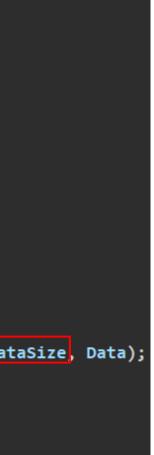
## n is triggered per



## **DXE GetVariable vuln search: results**

```
char Data[424]; // [rsp+40h] [rbp-1A8h] BYREF
UINTN DataSize; // [rsp+200h] [rbp+18h] BYREF
void *Interface; // [rsp+208h] [rbp+20h] BYREF
v8 = 1280i64;
DataSize = 0i64;
gBS_180007E38->AllocatePool(EfiBootServicesData, 0xA00ui64, a2);
v4 = *a2;
if ( *a2 < (char *)*a2 + 2 * v8 )
 do
   *v4++ = 0;
 while ( v4 < ( WORD *)((char *)*a2 + 2 * v8) );</pre>
gBS 180007E38->LocateProtocol(&EFI HII STRING PROTOCOL GUID 180007050, 0i64, &Interface);
v5 = gRT_180007E40->GetVariable((CHAR16 *)L"PlatformLang", &EFI_GLOBAL_VARIABLE_GUID_180006F20, 0i64, &DataSize, Data);
v5 = gRT 180007E40->GetVariable(
        (CHAR16 *)L'PlatformLang',
        &EFI GLOBAL VARIABLE GUID 180006F20,
        0i64,
        &DataSize,
        Data);
```







# efiXplorer SmmGetVariable vuln search





# efiXplorer: SmmGetVariable vuln search

- SmmGetVariable function from EFI\_SMM\_VARIABLE\_PROTOCOL
- functionality is like EFI\_RUNTIME\_SERVICES::GetVariable •

```
#ifndef __SMM_VARIABLE_H__
     10
          #define __SMM_VARIABLE_H__
     11
     12
          #define EFI_SMM_VARIABLE_PROTOCOL_GUID \
     13
     14
            { \
              0xed32d533, 0x99e6, 0x4209, { 0x9c, 0xc0, 0x2d, 0x72, 0xcd, 0xd9, 0x98, 0xa7 } \
     15
     16
            3
     17
          typedef struct _EFI_SMM_VARIABLE_PROTOCOL EFI_SMM_VARIABLE_PROTOCOL;
     18
     19
     20
          111
           /// EFI SMM Variable Protocol is intended for use as a means
          /// to store data in the EFI SMM environment.
     22
          111
     23
••• 24
         struct _EFI_SMM_VARIABLE_PROTOCOL {
            EFI_GET_VARIABLE
                                        SmmGetVariable;
     25
            EFI_GET_NEXT_VARIABLE_NAME SmmGetNextVariableName;
     26
     27
            EFI_SET_VARIABLE
                                        SmmSetVariable;
            EFI_QUERY_VARIABLE_INFO SmmQueryVariableInfo;
     28
     29
         - };
          extern EFI GUID gEfiSmmVariableProtocolGuid;
     31
     33 #endif
```

https://github.com/tianocore/edk2/blob/3806e1fd139775610d8f2e7541a916c3a91ad989/MdeModulePkg/Include/Protocol/SmmVariable.h#L24 @BLACKHATEVENTS







# efiXplorer: SmmGetVariable vuln search

## **Algorithm and implementation**

(similar to GetVariable vuln search)

- Ioop through all the pairs of **SmmGetVariable** calls and get the address of the DataSize stack variable on the first call
- check that the data size is not initialized before the second call to **SmmGetVariable**
- check that the **DataSize** argument variable is the same for two calls

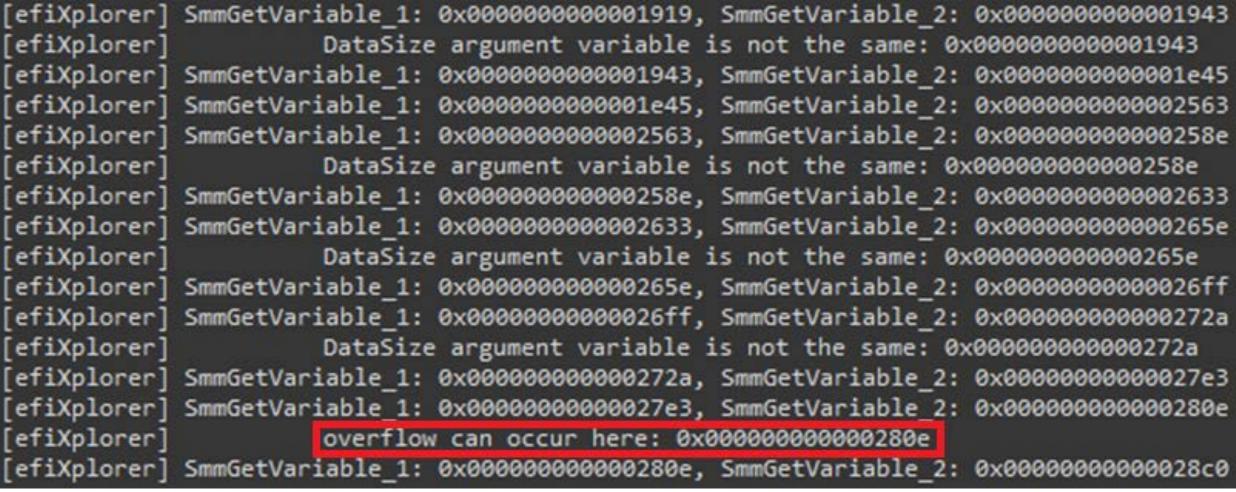
```
heck DataSize initialization */
bool init ok = false;
decode insn(&insn, prev head(curr addr, 0));
if (!(insn.itype == NN_mov && insn.ops[0].type == o_displ &&
      (insn.ops[0].phrase == REG_RSP
      insn.ops[0].phrase == REG_RBP))) {
    init ok = true;
  check that the DataSize argument variable is the same for two
  calls */
   (init ok) {
   ea = prev_head(static_cast<ea_t>(prev_addr), 0);
    for (auto i = 0; i < 10; ++i) {
        decode insn(&insn, ea);
       if (insn.itype == NN_lea && insn.ops[0].type == o_reg &&
            insn.ops[0].reg == REG_R9) {
            if (dataSizeStackAddr == insn.ops[1].addr) {
                smmGetVariableOverflow.push back(curr addr);
                DEBUG MSG(
                    "[%s] \toverflow can occur here: 0x%016x\n",
                    plugin name, curr addr);
                break;
            DEBUG MSG
                "[%s] \tDataSize argument variable is not the "
                "same: 0x%016x\n",
                plugin name, curr addr);
        ea = prev_head(ea, 0);
```





# efiXplorer: SmmGetVariable vuln search

Static analyzer messages in the IDA output window







# efiXplorer: SmmGetVariable vuln examples

- The sequence of multiple SmmGetVariable calls may cause the buffer overflow inside SMM
  - 1. First call is required to update DataSize value
  - 2. Second call trigger OOB write

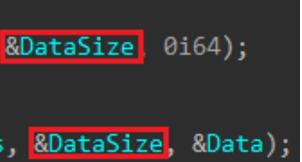
Dat	taSize = 7i64;
if	( (gSmmVar_3A48->SmmGetVariable(VariableName, &VendorGuid, 0i64, &DataSize, &Da
	(result = gSmmVar_3A48->SmmGetVariable(VariableName, &VendorGuid, 0i64, &Data
{	
0	Data = 0;
r	result = (gSmmVar_3A48->SmmSetVariable)(VariableName, &VendorGuid, 7i64, DataSiz

```
DataSize = 0i64;
result = gSmmVar_5B40->SmmGetVariable(VariableName, &VenorGuid, &Attributes, &DataSize
if ( result == EFI BUFFER_TOO_SMALL )
 result = gSmmVar_5B40->SmmGetVariable(VariableName, &VenorGuid, &Attributes, &DataSize, &Data);
```



aSize, &Data), result >= 0) )

ze, &Data);





## **SmmGetVariable vuln search: statistics**

Vendor Name	Avg number of calls per firmware	Avg number of vuln pattern firmware
ASRock	8	0
ASUS	7	0*
Lenovo	15	1

\* 3 cases among 820 firmware images





## n is triggered per



## **SmmGetVariable vuln search: results**

```
char v7[16]; // [rsp+30h] [rbp-29h] BYREF
char v8[24]; // [rsp+40h] [rbp-19h] BYREF
char v9[24]; // [rsp+58h] [rbp-1h] BYREF
char Data[64]; // [rsp+70h] [rbp+17h] BYREF
UINTN DataSize; // [rsp+C0h] [rbp+67h] BYREF
strcpy(v8, "M1 BIOS Is Enabled");
strcpy(v9, "M1 BIOS Is Disabled");
strcpy(v7, "Get Failed!");
sub_16B40(qword_226A0 + 2064, 1008i64, 0i64);
*(qword_226A0 + 2048) = 32;
DataSize = 1i64;
if ( (gSmmVar_226A8->SmmGetVariable(aSystem, &stru_16CE0, 0i64, &DataSize, Data) & 0x800000000000000000 == 0i64 )
 v0 = 0;
else
```







# efiXplorer PPI GetVariable vuln search

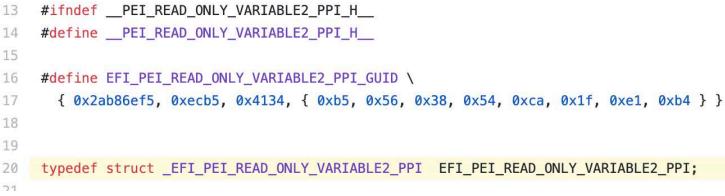




## efiXplorer: PPI GetVariable vuln search

## Similar to GetVariable in SMM, PEI modules rely on EFI\_PEI\_READ\_ONLY\_VARIABLE2\_PPI service to read nvram variables

	10
	16
	17
///	18
/// This PPI provides a lightweight, read-only variant of the full EFI	19
/// variable services.	20
///	21
<pre>struct _EFI_PEI_READ_ONLY_VARIABLE2_PPI {</pre>	
EFI_PEI_GET_VARIABLE2 GetVariable;	
EFI_PEI_GET_NEXT_VARIABLE_NAME2	
};	
<pre>extern EFI_GUID gEfiPeiReadOnlyVariable2PpiGuid;</pre>	
#endif	
	<pre>/// variable services. /// struct _EFI_PEI_READ_ONLY_VARIABLE2_PPI {     EFI_PEI_GET_VARIABLE2 GetVariable;     EFI_PEI_GET_NEXT_VARIABLE_NAME2 NextVariableName;   }; extern EFI_GUID gEfiPeiReadOnlyVariable2PpiGuid;</pre>



https://github.com/tianocore/edk2/blob/3806e1fd139775610d8f2e7541a916c3a91ad989/MdePkg/Include/Ppi/ReadOnlyVariable2.h#L104





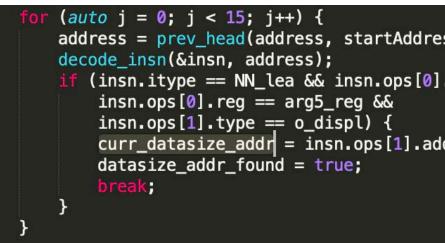


## efiXplorer: PPI GetVariable vuln search

## **Algorithm and implementation**

(similar to SmmGetVariable vuln search)

- loop through all the pairs of VariablePPI->GetVariable calls and get the address of the **DataSize** stack variable on the first call
- check that the **DataSize** argument is the same for both calls



```
for (auto j = 0; j < 15; j++) {</pre>
   address = prev_head(address, startAddress);
   decode_insn(&insn, address);
    if (insn.itype == NN_lea && insn.ops[0].type == o_reg &&
        insn.ops[0].reg == arg5_reg &&
        insn.ops[1].type == o_displ) {
        prev_datasize_addr = insn.ops[1].addr;
        datasize_addr_found = true;
        break;
```



.type == o_reg && dr;
dr;



## **PPI GetVariable vuln search: statistics**

Vendor Name	Avg number calls per firmware	Avg number of vuln pattern firmware
ASRock	122	12
ASUS	176	17
Lenovo	77	8



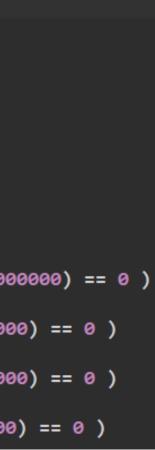


## n is triggered per



## **PPI GetVariable vuln search: results**

DataSize = 219;
v13 = 0xC885E881;
v16 = -85;
v17 = -73;
v18 = 77;
v19 = -34;
v20 = -84;
<pre>qmemcpy(v21, "V7(", sizeof(v21));</pre>
v25 = 0;
v24 = 0;
(*(v4 + 8))(PeiServices, &v13, 0, 0, &v25);
(*v3)->LocatePpi(v3, &EFI_PEI_READ_ONLY_VARIABLE2_PPI_GUID_FFF78100, <u>0, 0, &amp;Pe</u> iServices);
if ( ((*PeiServices)(PeiServices, L"SR5690ASetup", &VariableGuid, 0, &DataSize, Data) & 0x8000
*a3 = 1;
if ( ((*PeiServices)(PeiServices, L"SR5690BSetup", &v11, 0, &DataSize, Data + 219) & 0x8000000
a3[1] = 1;
if ( ((*PeiServices)(PeiServices, L"SR5690CSetup", &v10, 0, &DataSize, Data + 438) & 0x8000000
a3[2] = 1;
if ( ((*PeiServices)(PeiServices, L"SR5690DSetup", &v9, 0, &DataSize, Data + 657) & 0x80000000







# Vuln hunting at scale: results and statistics





## **Vuln hunting at scale: vendor stats**

## Attack surface and potential vulnerabilities: average numbers per 1 firmware for each of the 3 vendors

Vendor name	SMI handlers num.	Potential SMM callouts num.	PEI GetVariable calls num.	Potential PEI GetVariable vuln num	DXE GetVariable calls num.	Potential DXE GetVariable vuln num	SMM GetVariable calls num.	Potential SMM GetVariable vuln num
ASRock	51	72	122	12	735	2	8	0
ASUS	42	80	176	17	697	5	7	0.003
Lenovo	20	3	78	8	466	2	15	1





## **Vuln hunting at scale: Attack Surface stats**

Attack surface and potential vulnerabilities: average numbers per 1 firmware for each boot phase (PEI/SMM/DXE)

Metric	PEI	SMM	DX
GetVariable	152.00	8.00	695
GetVar Vuln	15.00	0.06	4.0







# efiXplorer: future plans







# Decompiler

## gRT\_2778->GetVariable)(aCnfg, &guid, &attributes, &size, data)

## Disassembly

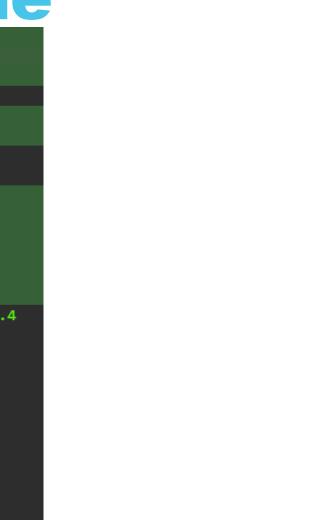
lea	rax, [rsp+15F8h+data]
lea	r9, [rsp+15F8h+size]
lea	r8, [r11+20h]
mov	[rsp+15F8h+Data], rax
mov	rax, cs:gRT_2778
lea	rdx, guid
lea	rcx, aCnfg ; "CNFG"
mov	r13d, 0EBA4h
mov	r14d, 4BB5h
xor	esi, esi
mov	<pre>[rsp+15F8h+VendorGuid.Data1], 0EC87D643h</pre>
mov	[rsp+15F8h+VendorGuid.Data4], 0A1h ; 'i'
mov	<pre>[rsp+15F8h+VendorGuid.Data2], r13w</pre>
mov	[rsp+15F8h+VendorGuid.Data3], r14w
mov	[rsp+15F8h+VendorGuid.Data4+1], ØE5h ; 'å'
mov	<pre>[rsp+15F8h+VendorGuid.Data4+2], 3Fh ; '?'</pre>
mov	<pre>[rsp+15F8h+VendorGuid.Data4+3], 3Eh ; '&gt;'</pre>
mov	<pre>[rsp+15F8h+VendorGuid.Data4+4], 36h ; '6'</pre>
mov	<pre>[rsp+15F8h+VendorGuid.Data4+5], 0B2h ; '2'</pre>
mov	[rsp+15F8h+VendorGuid.Data4+6], 0Dh
mov	<pre>[rsp+15F8h+VendorGuid.Data4+7], 0A9h ; '@'</pre>
mov	[r11+18h], esi
call	qword ptr [rax+48h]

## Microcode

add	rsp.8, #0x48.8, r9.8
add	r11.8, #0x20.8, r8.8
add	rsp.8, #0x60.8, %Data.8
mov	\$gRT_2778.8, rax.8
mov	&(\$guid).8, rdx.8
mov	&(\$aCnfg).8, rcx.8
mov	#0xEBA4.8, r13.8
mov	#0x4BB5.8, r14.8
mov	#0.1, cf.1
mov	#0.1, of.1
mov	#1.1, zf.1
setp	#0.4, #0.4, pf.1
mov	#0.1, sf.1
mov	#0.8, rsi.8
mov	#0xEC87D643.4, %VendorGuid.4
mov	#0xA1.1, %VendorGuid@8.1
mov	#0xEBA4.2, %VendorGuid@4.2
mov	#0x4BB5.2, %VendorGuid@6.2
mov	#0xE5.1, %VendorGuid@9.1
mov	
mov	#0x3E.1, %VendorGuid@11.1
mov	#0x36.1, %VendorGuid@12.1
mov	#0xB2.1, %VendorGuid@13.1
mov	#0xD.1, %VendorGuid@14.1
mov	#0xA9.1, %VendorGuid@15.1
	#0.4, ds.2, (r11.8+#0x18.8)
icall	cs.2, [ds.2:(rax.8+#0x48.8)]





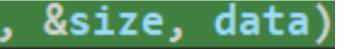




## T\_2778->GetVariable)(aCnfg, &guid, &attributes, &size, data)

mov	#0xEBA4.8, r13.8	; 0000129F
mov	#0x4BB5.8, r14.8	; 000012A5
mov	#0.8, rsi.8{1}	; 000012AB
mov	#0xEC87D643.4, %VendorGuid.4	; 000012AD
mov	#0xA1.1, %VendorGuid@8.1	; 000012B5
mov	#0xEBA4.2, %VendorGuid@4.2	; 000012BA
mov	#0x4BB5.2, %VendorGuid@6.2	; 000012C0
mov	#0xE5.1, %VendorGuid@9.1	; 000012C6
mov	#0x3F.1, %VendorGuid@10.1	; 000012CB
mov	#0x3E.1, %VendorGuid@11.1	; 000012D0
mov	#0x36.1, %VendorGuid@12.1	; 000012D5
mov	#0xB2.1, %VendorGuid@13.1	; 000012DA
mov	#0xD.1, %VendorGuid@14.1	; 000012DF
mov	#0xA9.1, %VendorGuid@15.1	; 000012E4
mov	#0.4, %Attributes.4	; 000012E9
mov		3.8+#0x48.8)].8 <fast:_qword &(\$acnfg).8,_qword="" &(\$guid).8,_qword="" &(%attributes).8,_qword="" &(%size).8,_qw<="" td=""></fast:_qword>









## **Power of dataflow analysis**

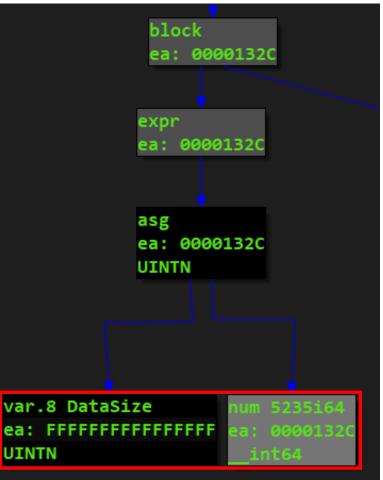
## gRT\_2778->GetVariable)(aCnfg, &guid, &attributes, &size, data



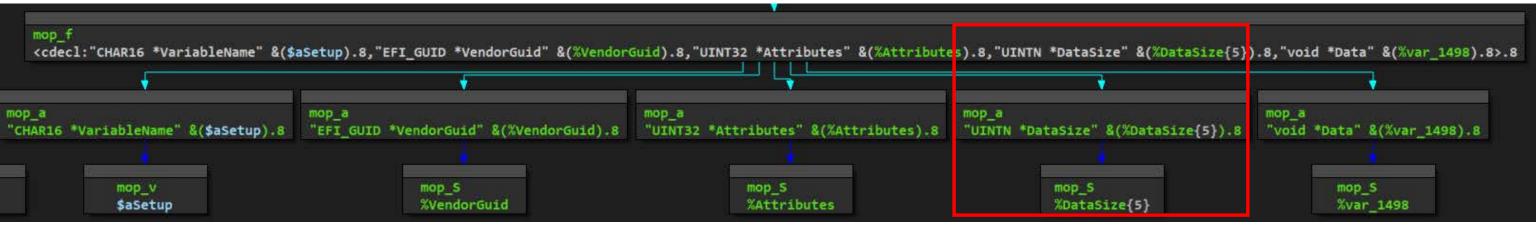
UINTN















## Conclusion

- Well-tuned heuristics work surprisingly well for UEFI security analysis  $\checkmark$ 
  - recovery of important structures
  - automated attack surface measurement (!)
  - automated potential vulnerability finding (!)
- ✓ Firmware vendors have worked on attack surface reduction, but well-known attack vectors is still a problem in 2020, such as: SMM callouts, GetVariable misuse
- $\checkmark$  We need more open, usable, and working instruments for UEFI security, including: Vuln research, RE and automation, Forensics and Data Science
- ✓ It's about right time for a much broader audience to look into the problem of UEFI implants
- ✓ Who knows what else we'll find there?



## **blackhat** EUROPE 2020

DECEMBER 9-10 BRIEFINGS

# Thank you

# @matrosov, @isciurus, @yeggorv, @p4111